## MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

## H-PSOF8L 11.68x9.80x2.30, 1.20P CASE 100CU ISSUE E DATE 31 MAY 2024 (2x) \_ ccc D -В 6.64 0.80 (2X) D2 (2x) TERMINAL 1 CORNER A ∕₅∖ 4.20 -/7\ 10.20 8.00 h1 F (DATUM A) 4.60 E2 b (8x) 2.80 8.10 (DATUM B) bbb C A B D4 (2x) $\oplus$ 2.38 ddd M C Lei 6 – L2 (8x) F2'(2x)-L1 6 LAND PATTERN RECOMMENDATION SECTION "A-A" \*FOR ADDITIONAL INFORMATION ON OUR PB-FREE SCALE: 2X STRATEGY AND SOLDERING DETAILS, PLEASE TOP VIEW DOWNLOAD THE ONSEMI SOLDERING AND MOUNTING DETAIL "B' TECHNIQUES REFERENCE MANUAL, SOLDERRM/D. NOTES: // aaa C 1. PACKAGE STANDARD REFERENCE: JEDEC MO-299, ISSUE B. 2. DIMENSIONING AND TOLERANCING PER ASME Y14-5M, 2018. A1 ⁄4 A 3. "e" REPRESENTS THE TERMINAL PITCH. 4. THIS DIMENSION INCLUDES ENCAPSULATION THICKNESS "A1", AND PACKAGE BODY THICKNESS, BUT DOES NOT INCLUDE c SIDE VIEW ATTACHED FEATURES, e.g., EXTERNAL OR CHIP CAPACITORS. AN INTEGRAL HEATSLUG IS NOT CONSIDERED AS ATTACHED FEATURE. 5. A VISUAL INDEX FEATURE MUST BE LOCATED WITHIN THE ☐ ccc (2x) D1 HATCHED AREA. 6. DIMENSIONS b1.L1.L2 APPLY TO PLATED TERMINALS D5 (2x) DETAIL "B" 7. THE LOCATION AND SIZE OF EJECTOR MARKS ARE OPTIONAL. SCALE: 2X D6 8. THE LOCATION AND NUMBER OF FUSED LEADS ARE OPTIONAL. D3 (2x) (2x) MILLIMETERS MILLIMETERS L3 DIM DIM MIN. MAX MIN. NOM. MAX. NOM Α 2.20 2.30 2.40 F5 9.36 9.46 9.47 8-A1 1.70 1.80 1.90 E6 1.10 1.20 1.30 F6 0 70 0.80 0.90 E7 0.15 0.18 0.21 h (DATUM A) €b1 9.70 9.80 9.90 1.20 BSC (3x) е E1 E3 E4 E5 0.35 0.55 b2 0.45 e/2 0.60 BSC √<sup>b2 (8x)</sup> 0.40 0.50 0.60 н 11.58 11.68 11.78 с D 10.28 10.38 10.48 H/2 5.74 5.84 5.94 D/2 5.09 5.19 5.29 H1 7.15 BSC D1 10.98 11.08 11.18 1 90 2.00 2 10 1 /8\ D2 3 20 3.30 3 40 L1 0.60 0.70 0.80 HEAT SLUG TERMINAL D/2 L (8x) D3 2.80 2.60 2.70 L2 0.50 0.60 0.70 D4 4.45 4.55 4.65 L3 0.70 0.80 0.90 H/2 (DATUM B) D5 3.20 3.30 3.40 θ 10° REF D6 0.55 0.65 0.75 θ1 10° REF H1 Е 9.80 9.90 10.00 aaa 0.20 BOTTOM VIEW E1 7.30 7.40 7.50 bbb 0.25 GENERIC E2 0.30 0.40 0.50 ccc 0.20 E3 7.40 7.50 7.60 ddd 0.20 **MARKING DIAGRAM\*** 8.40 E4 8.20 8.30 eee 0 10 AYWWZZ Α = Assembly Location \*This information is generic. Please refer to Y = Year device data sheet for actual part marking. ww = Work Week Pb-Free indicator, "G" or microdot "•", may XXXXXXXX = Assembly Lot Code ZZ or may not be present. Some products may XXXXXXXX XXXX = Specific Device Code not follow the Generic Marking. Electronic versions are uncontrolled except when accessed directly from the Document Repository. DOCUMENT NUMBER: 98AON13813G Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. **DESCRIPTION:** H-PSOF8L 11.68x9.80x2.30, 1.20P PAGE 1 OF 1 onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular

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