

Automotive 750 V, 640 A Single Side Direct Cooling 6-Pack Power Module

VE-Trac™ Direct Module NVH640S75L4SPB

Product Description

The NVH640S75L4SPB is a power module from the VE-Trac™ Direct family of highly integrated power modules with industry standard footprints for Hybrid (HEV) and Electric Vehicle (EV) traction inverter application.

The module integrates four Field Stop 4 (FS4) 750 V Narrow Mesa IGBTs in a 6-pack configuration, which excels in providing high current density, while offering robust short circuit protection and increased blocking voltage. Additionally, FS4 750 V Narrow Mesa IGBTs show low power losses during lighter loads, which helps to improve overall system efficiency in automotive applications.

For assembly ease and reliability, a new generation of press-fit pins are integrated into the power module signal terminals. In addition, the power module has an optimized pin-fin heatsink in the baseplate.

Features

- Direct Cooling w/ Integrated Pin-fin Heatsink
- Ultra-low Stray Inductance
- $T_{vjmax} = 175$ °C Continuous Operation
- Low V_{CESAT} and Switching Losses
- Automotive Grade FS4 750 V Narrow Mesa IGBT
- Fast Recovery Diode Chip Technologies
- 4.2 kV Isolated DBC Substrate
- Easy to Integrate 6-pack Topology
- This Device is Pb-Free and is RoHS Compliant

Typical Applications

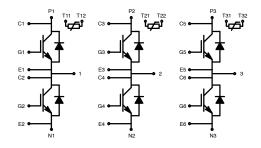
- Hybrid and Electric Vehicle Traction Inverter
- High Power Converters



SSDC33, 154.50x92.0 (SPB) CASE 183AB

MARKING DIAGRAM

XXXXX = Specific Device Code
AT = Assembly & Test Site Code
YYWW = Year and Work Week Code



ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

Pin Description

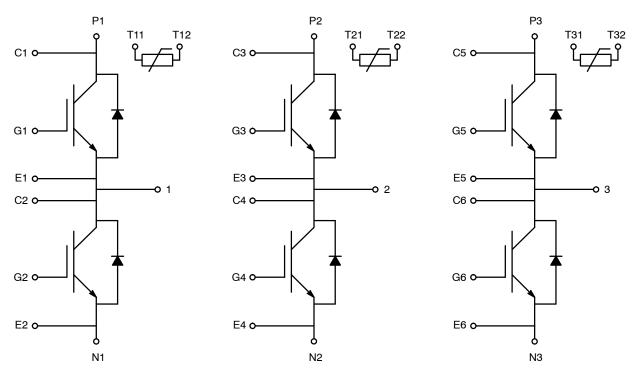


Figure 1. Pin Description

PIN FUNCTION DESCRIPTION

Pin #	Pin Function Description
P1, P2, P3	Positive Power Terminals
N1, N2, N3	Negative Power Terminals
1	Phase 1 Output
2	Phase 2 Output
3	Phase 3 Output
G1-G6	IGBT Gate
E1-E6	IGBT Gate Return
C1-C6	Desat Detect/Collector Sense
T11, T12	Phase 1 Temperature Sensor Output
T21, T22	Phase 2 Temperature Sensor Output
T31, T32	Phase 3 Temperature Sensor Output

Materials

DBC Substrate: Al₂O₃ isolated substrate, basic isolation,

and copper on both sides

Terminals: Copper + Tin electro-plating Signal Leads: Copper + Tin plating Pin-fin Base plate: Copper + Ni plating

Flammability Information

The module frame meets UL94V-0 flammability rating.

MODULE CHARACTERISTICS ($T_{vj} = 25^{\circ}C$, Unless Otherwise Specified)

Symbol	Parameter	Rating	Unit
T _{vj}	Operating Junction Temperature	-40 to 175	°C
T _{STG}	Storage Temperature	-40 to 125	°C
V _{ISO}	Isolation Voltage (DC, 0 Hz, 1 s)	4200	V
L _{sCE}	Stray Inductance	8	nΗ
RCC'+EE'	Module Lead Resistance, Terminals - Chip	0.8	mΩ
G	Module Weight	700	g
CTI	Comparative Tracking Index	>200	-
d _{creep}	Creepage: Terminal to Heatsink Terminal to Terminal	9.0 9.0	mm
d _{clear}	Clearance: Terminal to Heatsink Terminal to Terminal	4.5 4.5	mm

Symbol	Parameters	Conditions	Min	Тур	Max	Unit
Δр	Pressure Drop in Cooling Circuit	10 L/min, 65°C, 50/50 EGW	ı	95	-	mbar
P (Note 1)	Maximum Pressure in Cooling Loop (relative)	T _{Baseplate} < 40°C T _{Baseplate} > 40°C		1 -	2.5 2.0	bar

^{1.} EPDM rubber 50 durometer 'O' ring used.

ABSOLUTE MAXIMUM RATINGS (T_{vj} = 25°C, Unless Otherwise Specified)

Symbol	Parameter	Rating	Unit	
ВТ				
V _{CES}	Collector to Emitter Voltage		750	V
V_{GES}	Gate to Emitter Voltage		±20	V
I _{CN}	Implemented Collector Current		640	А
I _{C nom}	Continuous DC Collector Current, T _{vj} = 175°C, T _F =	inuous DC Collector Current, T _{vj} = 175°C, T _F = 65°C, Ref. Heatsink		
I _{CRM}	Pulsed Collector Current @ V _{GE} = 15 V, t _p =1 mS		1280	А
P _{tot}	Total Power Dissipation T _{vj} = 175°C, T _F = 65°C, Ref. Heatsink		680	W
ode				
V_{RRM}	Repetitive Peak Reverse Voltage		750	V
I _{FN}	Implemented Forward Current		640	Α
I _F	Continuous Forward Current, T _{vj} = 175°C, T _F = 65°C	C, Ref. Heatsink	250 (Note 2)	А
I _{FRM}	Repetitive Peak Forward Current, t _p = 1 ms		1280	А
I ² t value	Surge Current Capability, t _p = 10 ms,	T _{vj} = 150°C T _{vj} = 175°C	11200 8200	A ² s

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

2. Verified by characterization/design, not by test.

CHARACTERISTICS OF IGBT ($T_{vj} = 25^{\circ}C$, Unless Otherwise Specified)

Symbol	Parameters	Condition	s	Min	Тур	Max	Unit
V _{CESAT}	Collector to Emitter Saturation Voltage (Terminal)	V _{GE} = 15 V, I _C = 450 A	T _{vj} = 25°C	_	1.35	1.64	V
	Collector to Emitter Saturation Voltage (Chip)	V _{GE} = 15 V, I _C = 450 A	$T_{vj} = 25^{\circ}C$ $T_{vj} = 150^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	1.27 1.39 1.45	1.56 - -	
		V _{GE} = 15 V, I _C = 640 A	$T_{vj} = 25^{\circ}C$ $T_{vj} = 150^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	1.46 1.68 1.76	- - -	
I _{CES}	Collector to Emitter Leakage Current	V _{GE} = 0, V _{CE} = 750 V	$T_{vj} = 25^{\circ}C$ $T_{vj} = 150^{\circ}C$	- -	_ 2.0	500 -	μA mA
I _{GES}	Gate – Emitter Leakage Current	V _{CE} = 0, V _{GE} = ±20 V		_	_	±300	nA
V _{th}	Threshold Voltage	$V_{CE} = V_{GE}$, $I_C = 60 \text{ mA}$		4.8	5.5	6.6	٧
Q_{G}	Total Gate Charge	$V_{GE=}$ -8 to 15 V, V_{CE} = 400	V	-	1.8	-	μС
R _{Gint}	Internal Gate Resistance			-	2.5	-	Ω
C _{ies}	Input Capacitance	V _{CE} = 30 V, V _{GE} = 0 V, f = 1	00 kHz	-	47	-	nF
C _{oes}	Output Capacitance	V _{CE} = 30 V, V _{GE} = 0 V, f = 1	00 kHz	-	1.3	-	nF
C _{res}	Reverse Transfer Capacitance	V _{CE} = 30 V, V _{GE} = 0 V, f = 1	00 kHz	-	0.08	-	nF
$T_{d.on}$	Turn On Delay, Inductive Load	I_{C} = 450 A, V_{CE} = 400 V, V_{GE} = +15/-8 V, Rg.on = 2.8 Ω	$T_{vj} = 25^{\circ}C$ $T_{vj} = 150^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	231 241 240	- - -	ns
T _r	Rise Time, Inductive Load	I_{C} = 450 A, V_{CE} = 400 V, V_{GE} = +15/-8 V, Rg.on = 2.8 Ω	$T_{vj} = 25^{\circ}C$ $T_{vj} = 150^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	128 133 134	- - -	ns
$T_{d.off}$	Turn Off Delay, Inductive Load	I_{C} = 450 A, V_{CE} = 400 V, V_{GE} = +15/-8 V, Rg.off = 18 Ω	$T_{vj} = 25^{\circ}C$ $T_{vj} = 150^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	816 915 933	- - -	ns
T _f	Fall Time, Inductive Load	I_C = 450 A, V_{CE} = 400 V, V_{GE} = +15/-8 V, Rg.off = 18 Ω	$T_{vj} = 25^{\circ}C$ $T_{vj} = 150^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	140 194 207	- - -	ns
E _{ON}	Turn-On Switching Loss (Including Diode Reverse Recovery Loss)	I_{C} = 450 A, V_{CE} = 400 V, V_{GE} = +15/-8 V, Ls = 22 nH, Rg.on = 2.8 Ω	$\begin{aligned} &\text{di/dt} = 2.8 \text{ A/ns}, \\ &T_{vj} = 25^{\circ}\text{C} \\ &\text{di/dt} = 2.7 \text{ A/ns}, \\ &T_{vj} = 150^{\circ}\text{C} \end{aligned}$	-	21 27	-	mJ
			di/dt = 2.6 A/ns, T _{vj} = 175°C	-	29	-	
E _{OFF}	Turn-Off Switching Loss	I _C = 450 A, V _{CE} = 400 V, V _{GE} + 15/-8 V,	$dv/dt = 1.6 \text{ V/ns},$ $T_{vj} = 25^{\circ}\text{C}$	_	20	-	mJ
		Ls = 22 nH, Rg.off = 18 Ω	dv/dt = 1.3 V/ns, $T_{vj} = 150^{\circ}\text{C}$	_	27	_	
			dv/dt = 1.3 V/ns, $T_{vj} = 175^{\circ}\text{C}$	-	29	-	
E _{SC}	Minimum Short Circuit Energy Withstand	V _{GE} = 15 V, V _{CC} = 400 V	$T_{vj} = 25^{\circ}C$ $T_{vj} = 175^{\circ}C$	5 3	_	-	J

$\textbf{CHARACTERISTICS OF INVERSE DIODE} \ (T_{vj} = 25^{\circ}\text{C}, \ \text{Unless Otherwise Specified})$

Symbol	Parameters	Condition	s	Min	Тур	Max	Unit
V _F	Diode Forward Voltage (Terminal)	I _F = 450 A	T _{vj} = 25°C	ı	1.70	2.02	V
	Diode Forward Voltage (Chip)	I _F = 450 A	$T_{vj} = 25^{\circ}C$ $T_{vj} = 150^{\circ}C$ $T_{vj} = 175^{\circ}C$		1.59 1.54 1.51	1.91 - -	
		I _F = 640 A	$T_{vj} = 25^{\circ}C$ $T_{vj} = 150^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	1.79 1.80 1.81	- - -	
E _{rr}	Reverse Recovery Energy	I_F = 450 A, V_R = 400 V, V_{GE} = -8 V, $Rg.on$ = 2.8 Ω	$\begin{aligned} &\text{di/dt} = 2.8 \text{ A/ns,} \\ &T_{vj} = 25^{\circ}\text{C} \\ &\text{di/dt} = 2.7 \text{ A/ns,} \\ &T_{vj} = 150^{\circ}\text{C} \\ &\text{di/dt} = 2.6 \text{ A/ns,} \\ &T_{vj} = 175^{\circ}\text{C} \end{aligned}$	-	2.3 6.1 7.3	-	mJ
Q _{RR}	Recovered Charge	I_F = 450 A, V_R = 400 V, V_{GE} = +15/-8 V, $Rg.on$ = 2.8 Ω	$\begin{aligned} &\text{di/dt} = 2.8 \text{ A/ns,} \\ &T_{vj} = 25^{\circ}\text{C} \\ &\text{di/dt} = 2.7 \text{ A/ns,} \\ &T_{vj} = 150^{\circ}\text{C} \\ &\text{di/dt} = 2.6 \text{ A/ns,} \\ &T_{vj} = 175^{\circ}\text{C} \end{aligned}$	- - -	9.6 23.6 27.6	-	μC
Irr	Peak Reverse Recovery Current	I_F = 450 A, V_R = 400 V, V_{GE} = -8 V, Rg.on = 2.8 Ω	$\begin{aligned} &\text{di/dt} = 2.8 \text{ A/ns,} \\ &T_{vj} = 25^{\circ}\text{C} \\ &\text{di/dt} = 2.7 \text{ A/ns,} \\ &T_{vj} = 150^{\circ}\text{C} \\ &\text{di/dt} = 2.6 \text{ A/ns,} \\ &T_{vj} = 175^{\circ}\text{C} \end{aligned}$	-	115 185 197	-	A

NTC SENSOR CHARACTERISTICS (T_{vj} = 25°C, Unless Otherwise Specified)

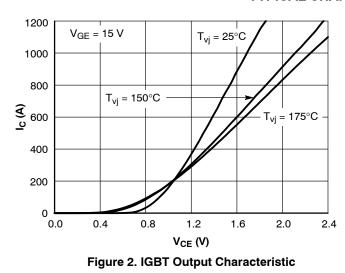
Symbol	Parameters	Conditions	Min	Тур	Max	Unit
R ₂₅ (Note 3)	Rated Resistance	T _C = 25°C	-	5	-	Ω
ΔR/R	Deviation of R100	$T_{C} = 100^{\circ}\text{C}, R_{100} = 493 \ \Omega$	5	_	5	%
P ₂₅	Power Dissipation	T _C = 25°C	-	_	20	mW
B _{25/50}	B-Value	$R = R_{25} \exp \left[B_{25/50} \left(1/T - 1/298 \right) \right]$	-	3375	-	K
B _{25/80}	B-Value	$R = R_{25} \exp \left[B_{25/80} \left(1/T - 1/298 \right) \right]$	-	3411	-	K
B _{25/100}	B-Value	$R = R_{25} \exp \left[B_{25/100} \left(1/T - 1/298 \right) \right]$	-	3433	-	K

THERMAL CHARACTERISTICS

Symbol	Parameter	Min	Тур	Max	Unit
IGBT.R _{th,J-F}	Rth, Junction to Fluid, 10 L/min, 65°C, 50/50 EGW	1	0.16	0.19	°C/W
Diode.R _{th,J-F}	Rth, Junction to Fluid, 10 L/min, 65°C, 50/50 EGW	-	0.28	0.33	°C/W

ORDERING INFORMATION

Part Number	Package	Shipping
NVH640S75L4SPB	SSDC33, 154.50x92.0 (SPB) (Pb-Free)	4 Units / Tray



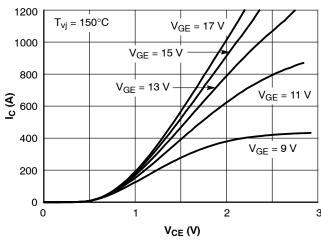


Figure 3. IGBT Output Characteristic

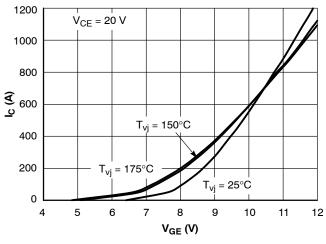


Figure 4. IGBT Transfer Characteristic

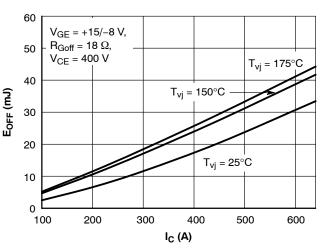


Figure 5. IGBT Turn-off Losses vs. I_C

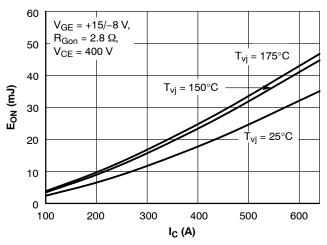


Figure 6. IGBT Turn-on Losses vs. I_C

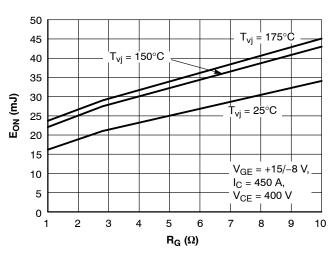
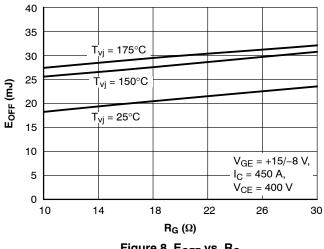


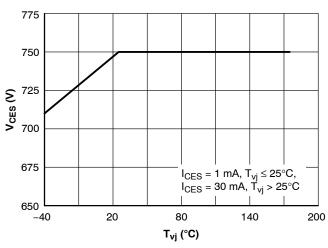
Figure 7. E_{ON} vs. R_G



15 10 V_{GE} (V) 0 $V_{CE} = 400 V$, -5 $I_C = 450 A$, T_{vj} = 25°C -100.0 0.2 0.4 0.6 0.8 1.2 Q_G (μC)

Figure 8. E_{OFF} vs. R_G

Figure 9. Gate Charge Characteristic



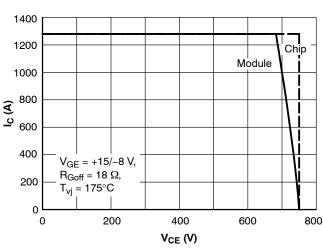
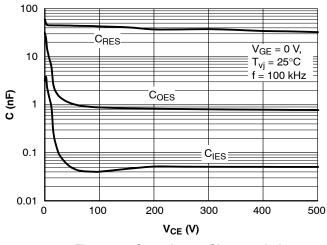


Figure 10. Maximum Allowed V_{CE}

Figure 11. Reverse Bias Safe Operating Area



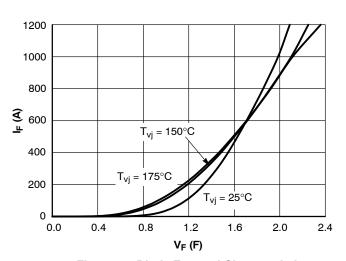
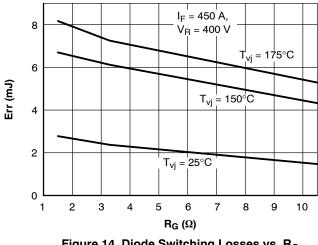


Figure 12. Capacitance Characteristic

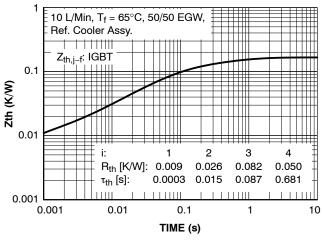
Figure 13. Diode Forward Characteristic



10 T_{vj} = 175 °C $R_{Gon} = 2.8 \Omega$ V_R = 400 V 8 $T_{vj} = 150^{\circ}C$ Err (mJ) 6 T_{vj} = 25^{\dot{0}}C 2 0 100 200 300 400 500 I_F (A)

Figure 14. Diode Switching Losses vs. R_G

Figure 15. Diode Switching Losses vs. IF



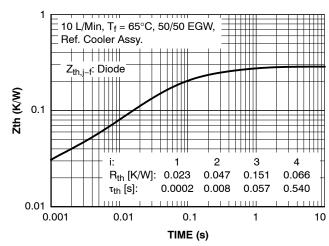
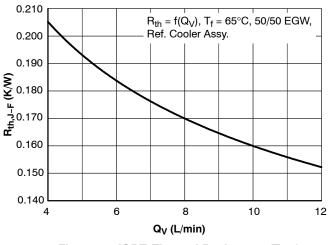


Figure 16. IGBT Transient Thermal Impedance (Typ.)

Figure 17. Diode Transient Thermal Impedance (Typ.)



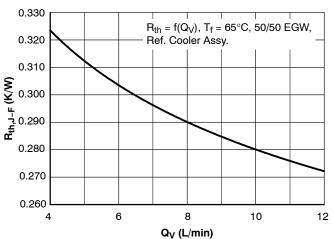
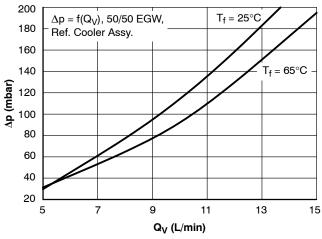


Figure 18. IGBT, Thermal Resistance (Typ.)

Figure 19. Diode, Thermal Resistance (Typ.)





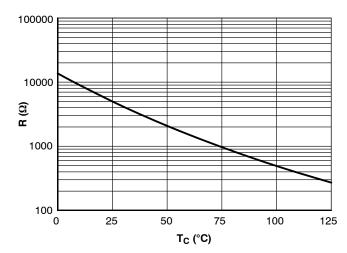
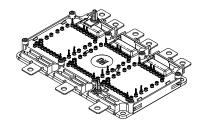
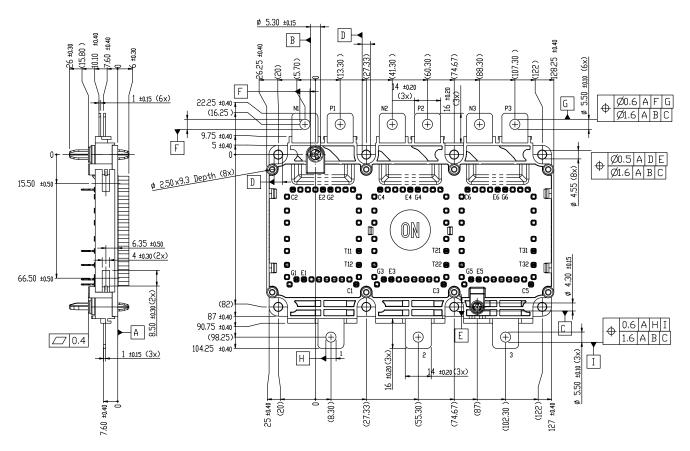


Figure 21. NTC Thermistor – Temperature Characteristic (Typical)



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DATE 05 DEC 2019



GENERIC MARKING DIAGRAM*

XXXXX = Specific Device Code

G = Pb-Free Package

AT = Assembly & Test Site Code

YYWW= Year and Work Week Code

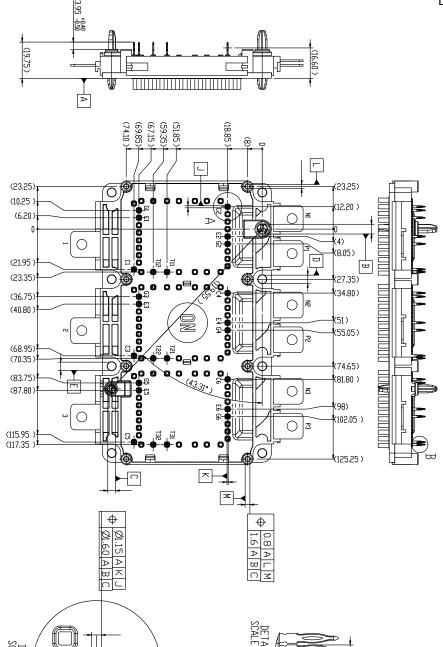
*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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