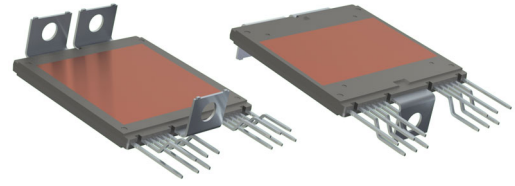


# Automotive 750 V, 800 A Dual Side Cooling Half-Bridge Power Module

## VE-Trac™ Dual Gen II NVG800A75L4DSC2



AHPM15-CEA  
 CASE MODHS

### Product Description

The NVG800A75L4DSC2 is part of a family of power modules with dual side cooling and compact footprints for Hybrid (HEV) and Electric Vehicle (EV) traction inverter application.

The module consists of two narrow mesa Field Stop (FS4) IGBTs in a half-bridge configuration. The chipset utilizes the new narrow mesa IGBT technology in providing high current density and robust short circuit protection with higher blocking voltage to deliver outstanding performance in EV traction applications.

Liquid cooling heatsink reference design, loss models and CAD models are available to support customers in inverter designs.

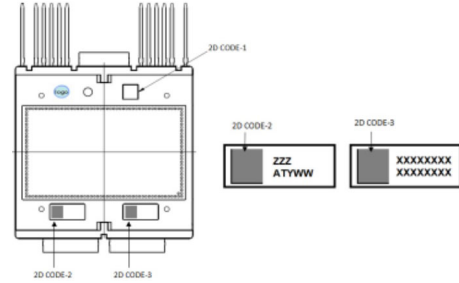
### Features

- Dual-Side Cooling
- Integrated Chip Level Temperature and Current Sensor
- $T_{vj\ max} = 175^{\circ}\text{C}$  for Continuous Operation
- Low-stray Inductance
- Low Conduction and Switching Losses
- Automotive Grade
- 4.2 kV Isolated DBC Substrate
- AEC Qualified and PPAP Capable
- This Device is Pb-Free and is RoHS Compliant

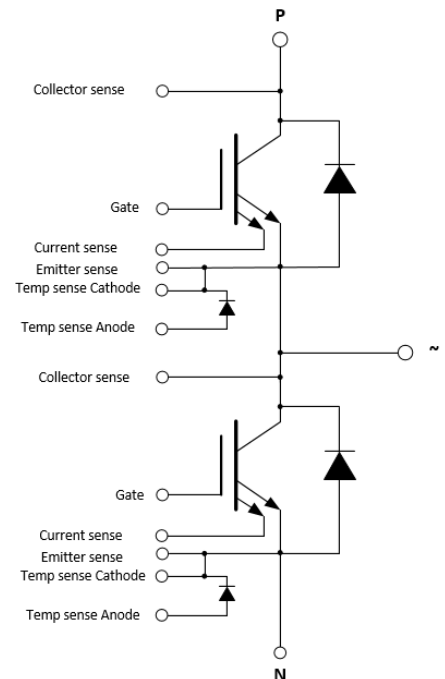
### Typical Applications

- Hybrid and Electric Vehicle Traction Inverter
- High Power DC-DC Converter

### MARKING DIAGRAM



ZZZ = Assembly Lot Code  
 AT = Assembly & Test Location  
 Y = Year  
 WW = Work Week  
 XXXX = Specific Device Code



### ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

# VE-Trac™ Dual Gen II NVG800A75L4DSC2

## PIN DESCRIPTION

Pin #	Pin	Pin Function Description	Pin Arrangement
1	N	Low Side Emitter	
2	P	High Side Collector	
3	H/S COLLECTOR SENSE	High Side Collector Sense	
4	H/S CURRENT SENSE	High Side Current Sense	
5	H/S EMITTER SENSE	High Side Emitter Sense	
6	H/S GATE	High Side Gate	
7	H/S TEMP SENSE (CATHODE)	High Side Temp sense Diode Cathode	
8	H/S TEMP SENSE (ANODE)	High Side Temp sense Diode Anode	
9	~	Phase Output	
10	L/S CURRENT SENSE	Low Side Current Sense	
11	L/S EMITTER SENSE	Low Side Emitter Sense	
12	L/S GATE	Low Side Gate	
13	L/S TEMP SENSE (CATHODE)	Low Side Temp sense Diode Cathode	
14	L/S TEMP SENSE (ANODE)	Low Side Temp sense Diode Anode	
15	L/S COLLECTOR SENSE	Low Side Collector Sense	

## Materials

DBC Substrate: Al<sub>2</sub>O<sub>3</sub> isolated substrate, basic isolation, and copper on both sides.

## Lead Frame

Copper with Tin electro-plating.

## Flammability Information

All materials present in the power module meet UL flammability rating class 94V-0.

## MODULE CHARACTERISTICS

Symbol	Parameter	Rating	Unit		
T <sub>vj</sub>	Continuous Operating Junction Temperature Range	-40 to 175	°C		
T <sub>STG</sub>	Storage Temperature range	-40 to 125	°C		
V <sub>ISO</sub>	Isolation Voltage, AC, f = 50 Hz, t = 1 s	4200	V		
Creepage	Minimum: Terminal to Terminal	5.0	mm		
Clearance	Minimum: (Note 1) Terminal to Terminal	3.2	mm		
CTI	Comparative Tracking Index	>600			
		<b>Min</b>	<b>Typ</b>	<b>Max</b>	
L <sub>sCE</sub>	Stray Inductance		8		nH
R <sub>CC'+EE'</sub>	Module Lead Resistance, Terminals - Chip		0.15		mΩ
G	Module Weight		75		g
M	M4 Screws for Module Terminals			2.2	Nm

1. Verified by design / not by test.

# VE-Trac™ Dual Gen II NVG800A75L4DSC2

## ABSOLUTE MAXIMUM RATINGS (T<sub>VJ</sub> = 25°C, Unless Otherwise Specified)

Symbol	Parameter	Rating	Unit
<b>IGBT</b>			
V <sub>CES</sub>	Collector to Emitter Voltage	750	V
V <sub>GES</sub>	Gate to Emitter Voltage	±20	V
I <sub>CN</sub>	Implemented Collector Current	800	A
I <sub>C nom</sub>	Continuous DC Collector Current, T <sub>VJmax</sub> = 175°C, T <sub>F</sub> = 65°C, ref. heatsink	550 <sup>(1)</sup>	A
I <sub>CRM</sub>	Pulsed Collector Current @ V <sub>GE</sub> = 15 V, t <sub>p</sub> = 1 ms	1600	A

## DIODE

V <sub>RRM</sub>	Repetitive peak reverse voltage	750	V
I <sub>FN</sub>	Implemented Forward Current	800	A
I <sub>F</sub>	Continuous Forward Current, T <sub>VJmax</sub> = 175°C, T <sub>F</sub> = 65°C, ref. heatsink	420 <sup>(1)</sup>	A
I <sub>FRM</sub>	Repetitive Peak Forward Current, t <sub>p</sub> = 1 ms	1600	A
I <sup>2</sup> t value	Surge current capability, V <sub>R</sub> = 0 V, t <sub>p</sub> = 10 ms, T <sub>VJ</sub> = 150°C T <sub>VJ</sub> = 175°C	20000 18000	A <sup>2</sup> s

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

2. Verified by characterization, not by test.

## THERMAL CHARACTERISTICS (Verified by characterization, not by test.)

Symbol	Parameter	Min	Typ	Max	Unit
IGBT.R <sub>th,J-C</sub>	Effective Rth, Junction to Case <sup>(3)</sup>		0.05	0.07	°C/W
IGBT.R <sub>th,J-F</sub>	Effective Rth, Junction to Fluid, λ <sub>TIM</sub> = 6 W/m-K, F = 660 N 10 L/min, 65°C, 50/50 EGW, Ref. Heatsink		0.128		°C/W
Diode.R <sub>th,J-C</sub>	Effective Rth, Junction to Case <sup>(3)</sup>		0.07	0.09	°C/W
Diode.R <sub>th,J-F</sub>	Effective Rth, Junction to Fluid, λ <sub>TIM</sub> = 6 W/m-K, F = 660 N 10 L/min, 65°C, 50/50 EGW, Ref. Heatsink		0.186		°C/W

3. For the measurement point of case temperature (T<sub>c</sub>), DBC discoloration, picker circle print is allowed, please refer to the VE-Trac Dual assembly guide for additional details about acceptable DBC surface finish.

## VE-Trac™ Dual Gen II NVG800A75L4DSC2

### CHARACTERISTICS OF IGBT (T<sub>vj</sub> = 25°C, Unless Otherwise Specified)

Parameters		Conditions	Min	Typ	Max	Unit
V <sub>CESAT</sub>	Collector to Emitter Saturation Voltage (Terminal)	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 600 A, T <sub>vj</sub> = 25°C T <sub>vj</sub> = 150°C T <sub>vj</sub> = 175°C  V <sub>GE</sub> = 15 V, I <sub>C</sub> = 800 A, T <sub>vj</sub> = 25°C T <sub>vj</sub> = 150°C T <sub>vj</sub> = 175°C	–	1.30 1.42 1.44  1.43 1.63 1.66	1.69	V
I <sub>CES</sub>	Collector to Emitter Leakage Current	V <sub>GE</sub> = 0, V <sub>CE</sub> = 750 V T <sub>vj</sub> = 25°C T <sub>vj</sub> = 175°C	– –	– 8	1 –	mA mA
I <sub>GES</sub>	Gate – Emitter Leakage Current	V <sub>CE</sub> = 0, V <sub>GE</sub> = ± 20 V	–	–	±400	nA
V <sub>th</sub>	Threshold Voltage	V <sub>CE</sub> = V <sub>GE</sub> , I <sub>C</sub> = 500 mA	4.5	5.5	6.5	V
Q <sub>G</sub>	Total Gate Charge	V <sub>GE</sub> = –8 to 15 V, V <sub>CE</sub> = 400 V	–	1.7	–	μC
R <sub>Gint</sub>	Internal gate resistance		–	2	–	Ω
C <sub>ies</sub>	Input Capacitance	V <sub>CE</sub> = 30 V, V <sub>GE</sub> = 0 V, f = 100 KHz	–	43	–	nF
C <sub>oes</sub>	Output Capacitance	V <sub>CE</sub> = 30 V, V <sub>GE</sub> = 0 V, f = 100 KHz	–	1.48	–	nF
C <sub>res</sub>	Reverse Transfer Capacitance	V <sub>CE</sub> = 30 V, V <sub>GE</sub> = 0 V, f = 100 KHz	–	0.19	–	nF
T <sub>d,on</sub>	Turn on delay, inductive load	I <sub>C</sub> = 600 A, V <sub>CE</sub> = 400 V T <sub>vj</sub> = 25°C V <sub>GE</sub> = +15/–8 V T <sub>vj</sub> = 150°C R <sub>g,on</sub> = 4.7 Ω T <sub>vj</sub> = 175°C	–	377 382 382	–	ns
T <sub>r</sub>	Rise time, inductive load	I <sub>C</sub> = 600 A, V <sub>CE</sub> = 400 V T <sub>vj</sub> = 25°C V <sub>GE</sub> = +15/–8 V T <sub>vj</sub> = 150°C R <sub>g,on</sub> = 4.7 Ω T <sub>vj</sub> = 175°C	–	104 127 132	–	ns
T <sub>d,off</sub>	Turn off delay, inductive load	I <sub>C</sub> = 600 A, V <sub>CE</sub> = 400 V T <sub>vj</sub> = 25°C V <sub>GE</sub> = +15/–8 V T <sub>vj</sub> = 150°C R <sub>g,off</sub> = 15 Ω T <sub>vj</sub> = 175°C	–	917 1042 1075	–	ns
T <sub>f</sub>	Fall time, inductive load	I <sub>C</sub> = 600 A, V <sub>CE</sub> = 400 V T <sub>vj</sub> = 25°C V <sub>GE</sub> = +15/–8 V T <sub>vj</sub> = 150°C R <sub>g,off</sub> = 15 Ω T <sub>vj</sub> = 175°C	–	129 199 212	–	ns
E <sub>ON</sub>	Turn-On Switching Loss (including diode reverse recovery loss)	I <sub>C</sub> = 600 A, V <sub>CE</sub> = 400 V, V <sub>GE</sub> = +15/–8 V, L <sub>s</sub> = 20 nH, R <sub>g,on</sub> = 4,7 Ω di/dt (T <sub>vj</sub> = 25°C) = 4.77 A/ns di/dt (T <sub>vj</sub> = 175°C) = 3.78 A/ns  T <sub>vj</sub> = 25°C T <sub>vj</sub> = 150°C T <sub>vj</sub> = 175°C	–	   22.93 35.87 37.70	–	mJ
E <sub>OFF</sub>	Turn-Off Switching Loss	I <sub>C</sub> = 600 A, V <sub>CE</sub> = 400 V, V <sub>GE</sub> = +15/–8 V, L <sub>s</sub> = 20 nH, R <sub>g,off</sub> = 15 Ω dv/dt (T <sub>vj</sub> = 25°C) = 2.79 V/ns dv/dt (T <sub>vj</sub> = 175°C) = 2.05 V/ns  T <sub>vj</sub> = 25°C T <sub>vj</sub> = 150°C T <sub>vj</sub> = 175°C	–	   33.57 47.30 49.09	–	mJ
E <sub>SC</sub>	Minimum Short Circuit Energy Withstand	V <sub>GE</sub> = 15 V, V <sub>CC</sub> = 400 V  T <sub>vj</sub> = 25°C T <sub>vj</sub> = 175°C	5	5		J

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

# VE-Trac™ Dual Gen II NVG800A75L4DSC2

## CHARACTERISTICS OF INVERSE DIODE (T<sub>VJ</sub> = 25°C, Unless Otherwise Specified)

Parameters		Conditions	Min	Typ	Max	Unit
V <sub>F</sub>	Diode Forward Voltage (Terminal)	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 600 A, T <sub>VJ</sub> = 25°C T <sub>VJ</sub> = 150°C T <sub>VJ</sub> = 175°C  V <sub>GE</sub> = 0 V, I <sub>C</sub> = 800 A, T <sub>VJ</sub> = 25°C T <sub>VJ</sub> = 150°C T <sub>VJ</sub> = 175°C	-	1.39 1.36 1.34  1.49 1.48 1.47	1.80	V
E <sub>rr</sub>	Reverse Recovery Energy	I <sub>F</sub> = 600 A, V <sub>R</sub> = 400 V, V <sub>GE</sub> = -8 V, R <sub>g,on</sub> = 4.7 Ω, -di/dt = 3.12 A/ns (175°C) T <sub>VJ</sub> = 25°C T <sub>VJ</sub> = 150°C T <sub>VJ</sub> = 175°C	-	6.05 14.89 17.12	-	mJ
Q <sub>RR</sub>	Recovered Charge	I <sub>F</sub> = 600 A, V <sub>R</sub> = 400 V, V <sub>GE</sub> = -8 V, R <sub>g,on</sub> = 4.7 Ω, -di/dt = 3.12 A/ns (175°C) T <sub>VJ</sub> = 25°C T <sub>VJ</sub> = 150°C T <sub>VJ</sub> = 175°C	-	17.12 44.69 52.25	-	μC
I <sub>rr</sub>	Peak Reverse Recovery Current	I <sub>F</sub> = 600 A, V <sub>R</sub> = 400 V, V <sub>GE</sub> = -8 V, R <sub>g,on</sub> = 4.7 Ω, -di/dt = 3.12 A/ns (175°C) T <sub>VJ</sub> = 25°C T <sub>VJ</sub> = 150°C T <sub>VJ</sub> = 175°C	-	222 311 325	-	A

## SENSOR CHARACTERISTICS (T<sub>VJ</sub> = 25°C, Unless Otherwise Specified)

Parameters		Conditions	Min	Typ	Max	Unit
T <sub>sense</sub>	Temperature sense	I <sub>F</sub> = 1 mA, T <sub>VJ</sub> = 25°C T <sub>VJ</sub> = 150°C T <sub>VJ</sub> = 175°C		2.5 1.7 1.5		V
I <sub>sense</sub>	Current sense	R <sub>shunt</sub> = 10 Ω, I <sub>C</sub> = 1600 A I <sub>C</sub> = 800 A I <sub>C</sub> = 100 A		505 269 50		mV

4. Measured at chip level

## ORDERING INFORMATION

Part Number	Package	Shipping
NVG800A75L4DSC2	AHPM15-CEA Module Case MODHS (Pb-Free)	18 Units / 3x Tube

# VE-Trac™ Dual Gen II NVG800A75L4DSC2

## TYPICAL CHARACTERISTICS

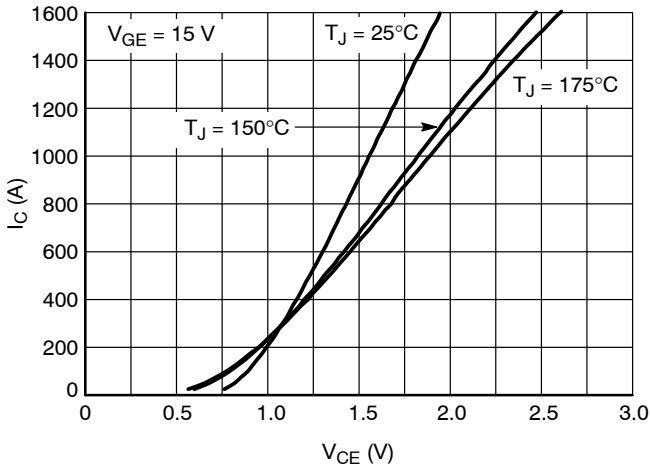


Figure 1. IGBT Output Characteristic

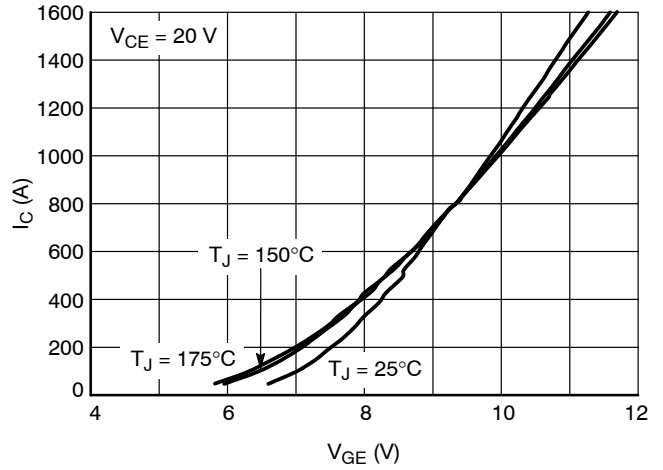


Figure 2. IGBT Transfer Characteristic

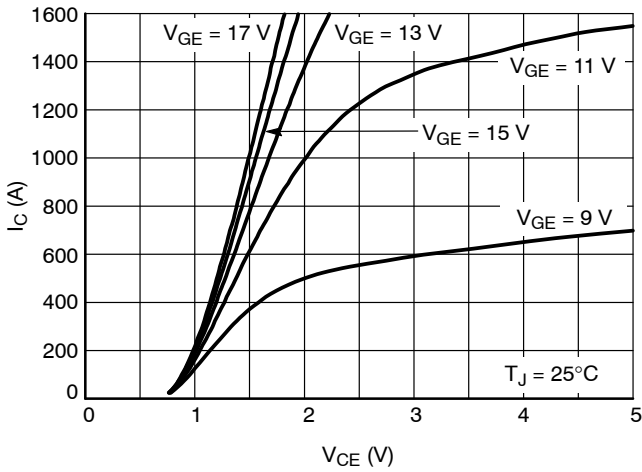


Figure 3. IGBT Output Characteristic, 25°C

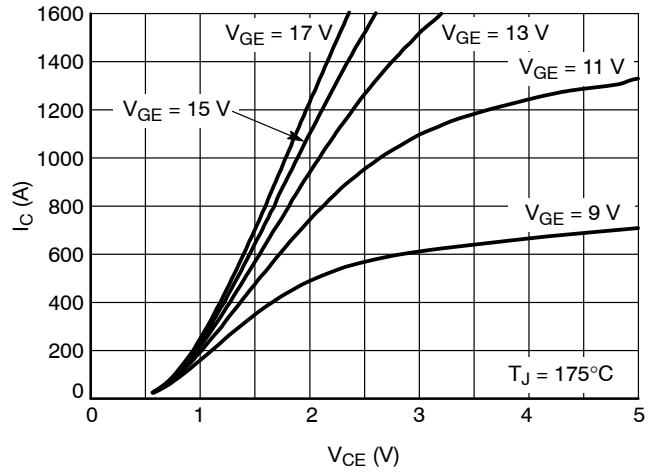


Figure 4. IGBT Output Characteristic, 175°C

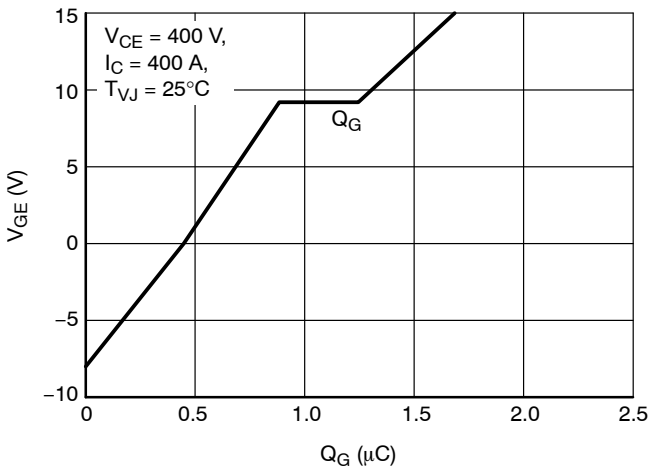


Figure 5. Gate Charge Characteristics

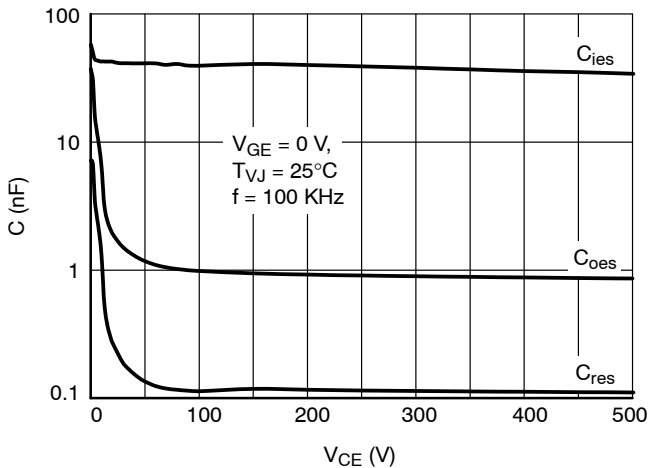


Figure 6. Capacitance Characteristics

# VE-Trac™ Dual Gen II NVG800A75L4DSC2

## TYPICAL CHARACTERISTICS

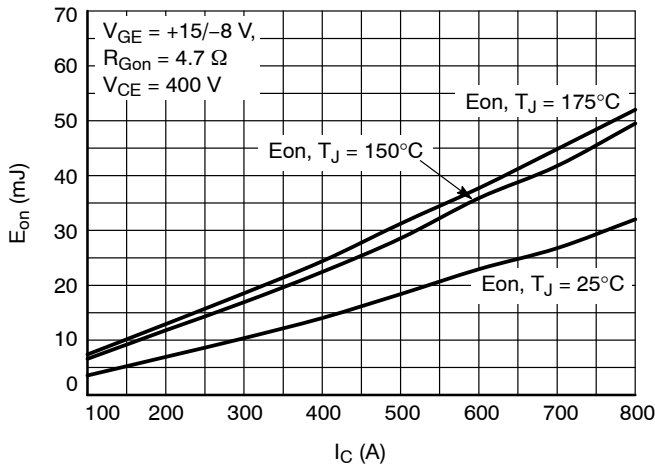


Figure 7.  $E_{on}$  vs.  $I_C$

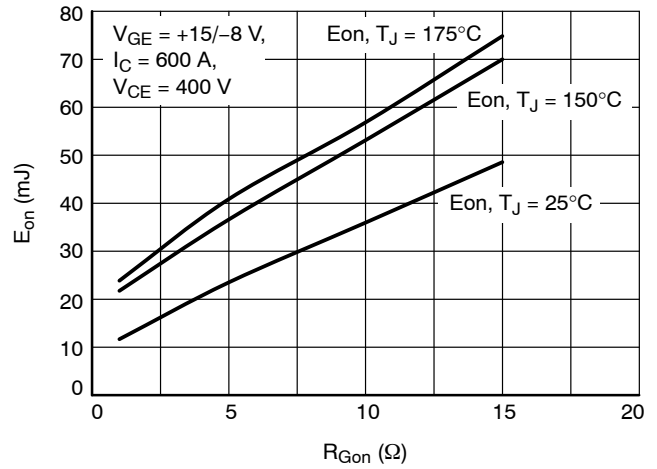


Figure 8.  $E_{on}$  vs.  $R_{Gon}$

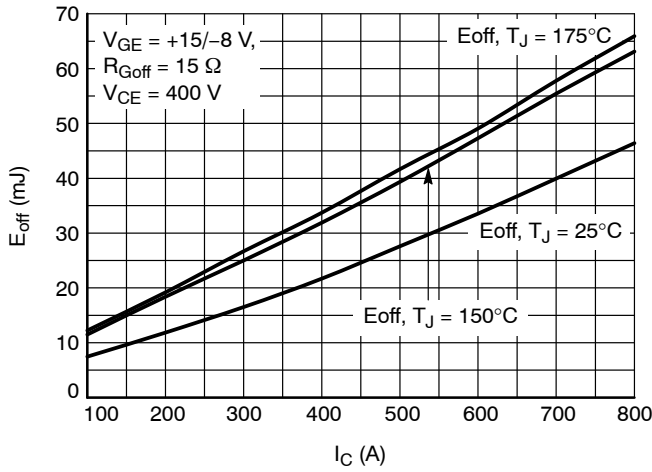


Figure 9.  $E_{off}$  vs.  $I_C$

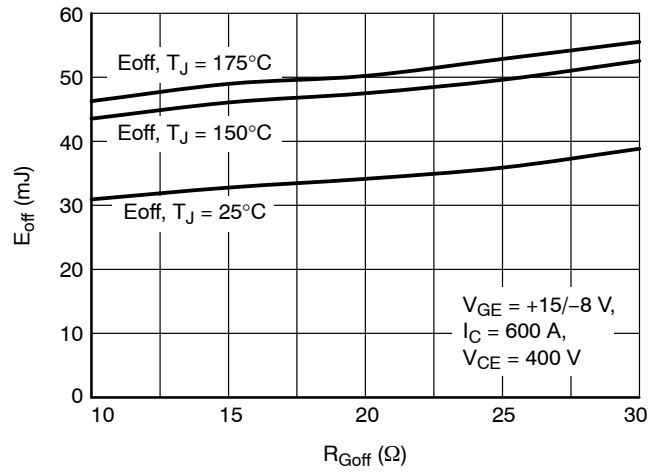


Figure 10.  $E_{off}$  vs.  $R_{Goff}$

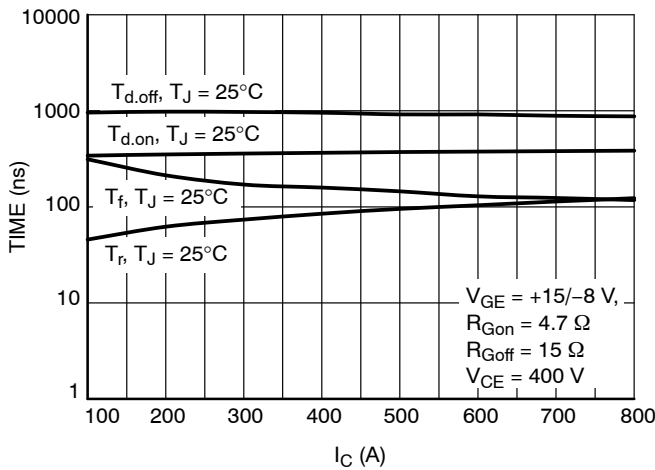


Figure 11. IGBT Switching Times vs.  $I_C$ ,  $T_{VJ} = 25^\circ\text{C}$

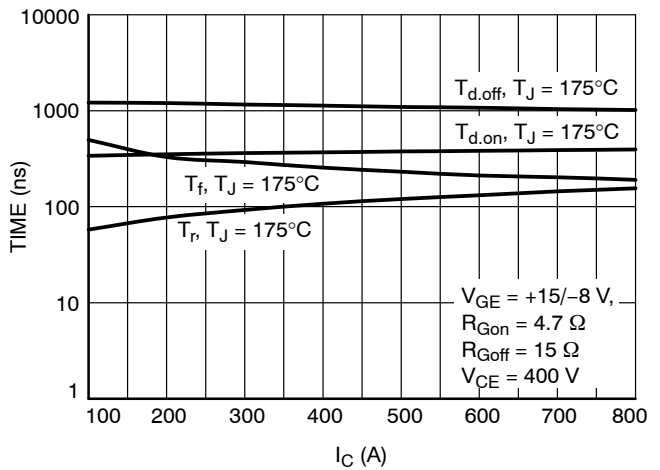


Figure 12. IGBT Switching Times vs.  $I_C$ ,  $T_{VJ} = 175^\circ\text{C}$

# VE-Trac™ Dual Gen II NVG800A75L4DSC2

## TYPICAL CHARACTERISTICS

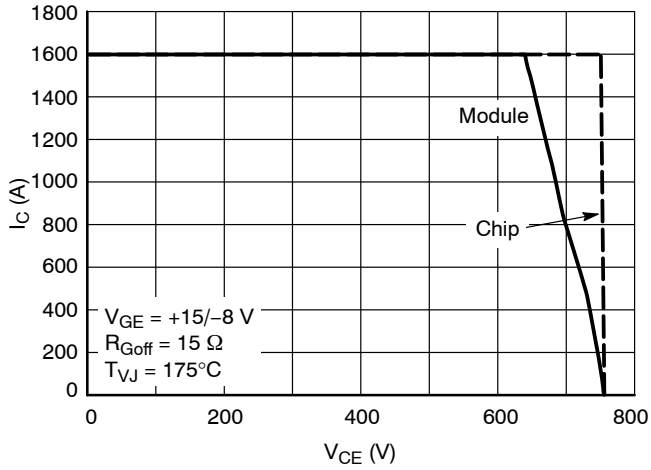


Figure 13. Reverse Bias Safe Operating Area

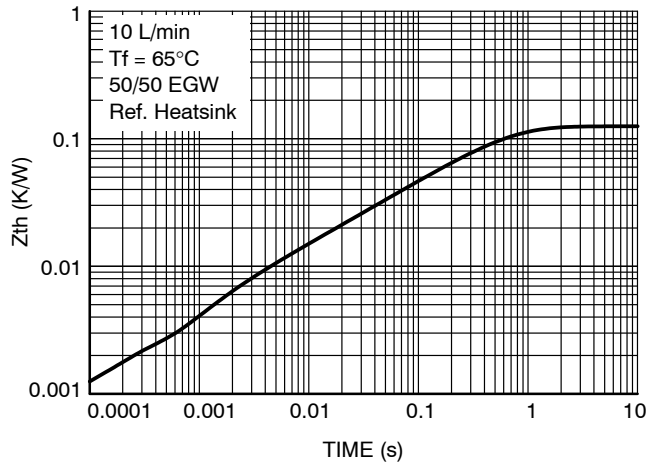


Figure 14. IGBT Transient Thermal Impedance

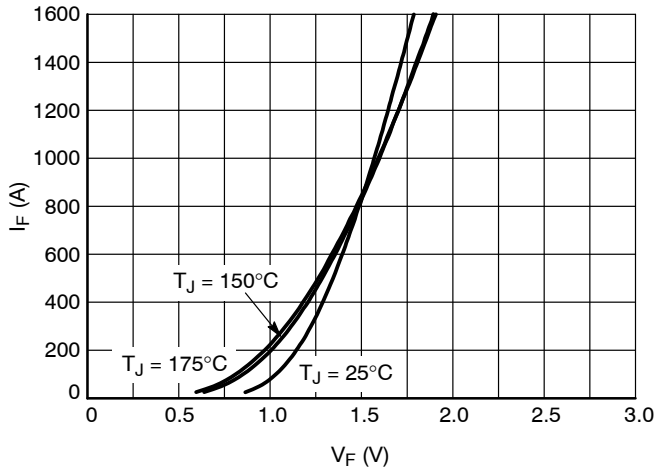


Figure 15. Diode Forward Characteristic

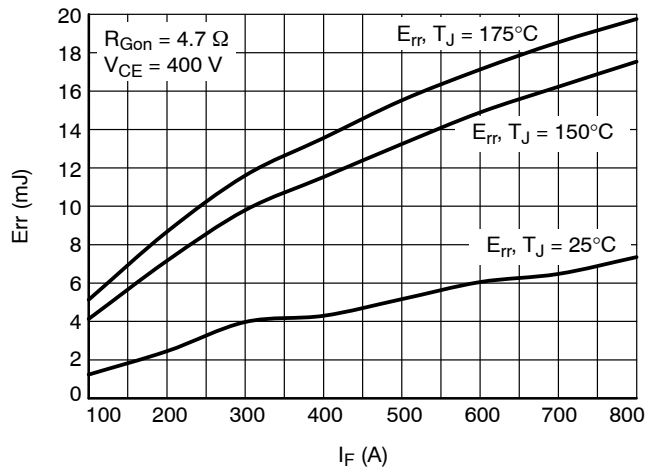


Figure 16. Diode Switching Losses vs.  $I_F$

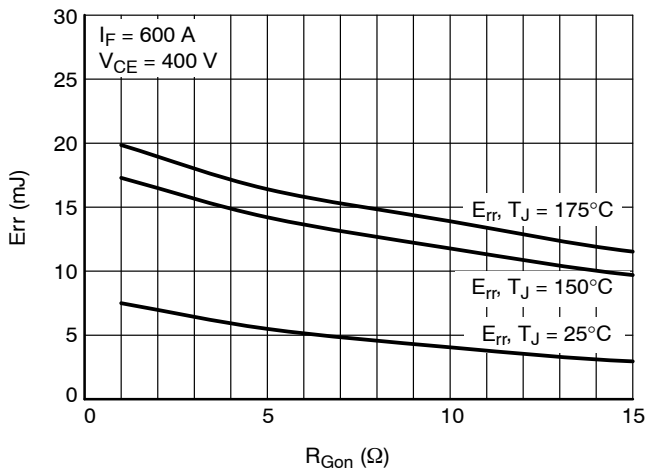


Figure 17. Diode Switching Losses vs.  $R_{GoN}$

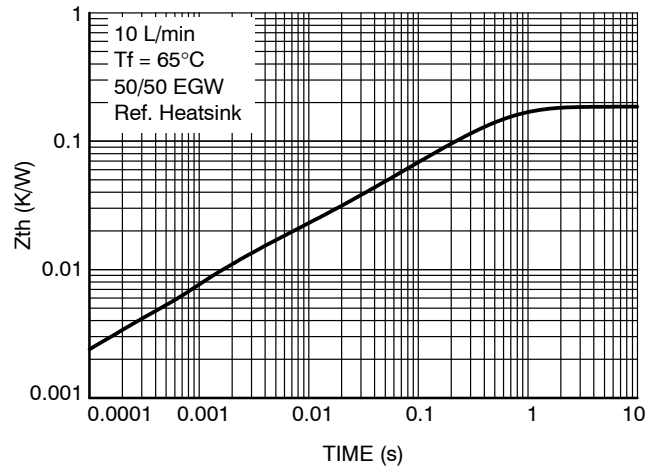


Figure 18. Diode Transient Thermal Impedance



# VE-Trac™ Dual Gen II NVG800A75L4DSC2

## TYPICAL CHARACTERISTICS

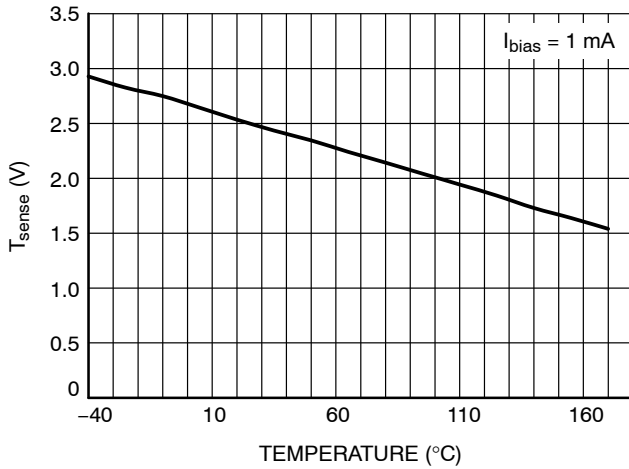


Figure 19. Temperature Sensor Characteristics

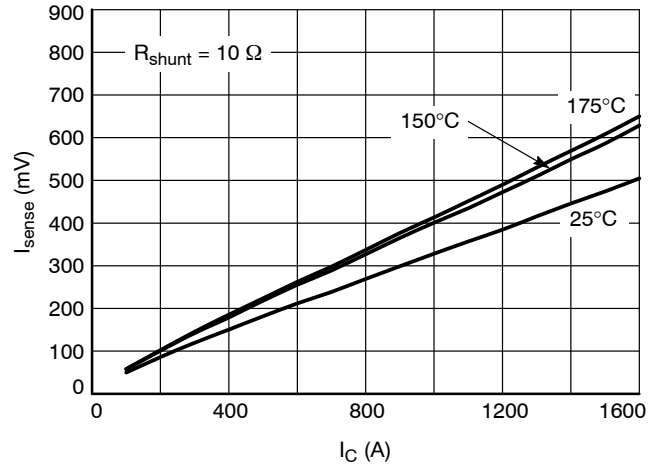


Figure 20. Current Sensor Characteristic

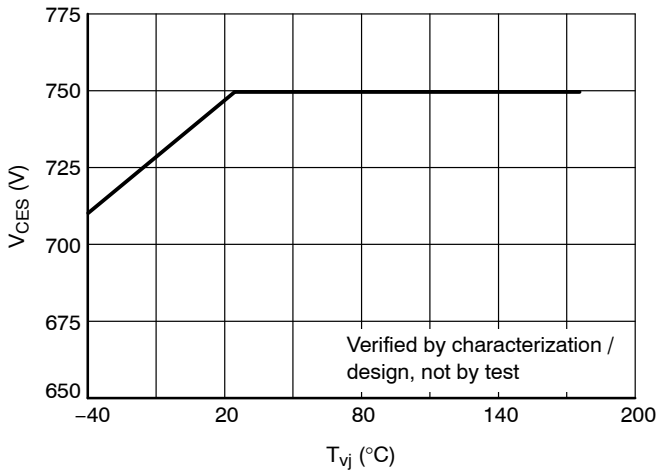
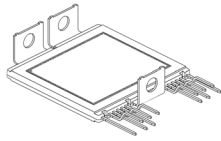


Figure 21. Maximum Allowed  $V_{CE}$

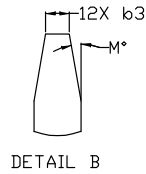
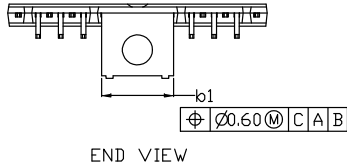
General Note: These are preliminary values measured from a small number of DV units. Values will be updated based on higher quantity of PV measurements.

# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



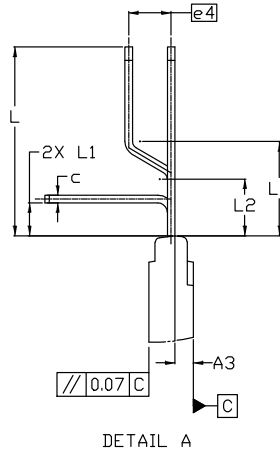
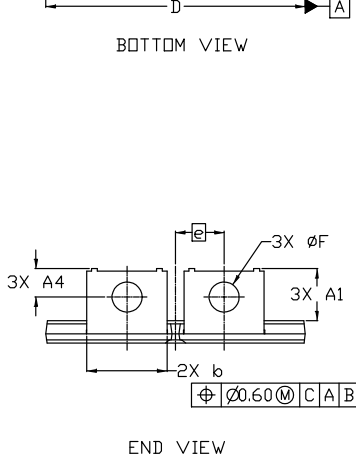
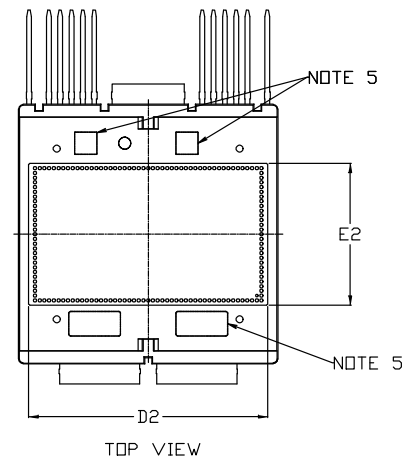
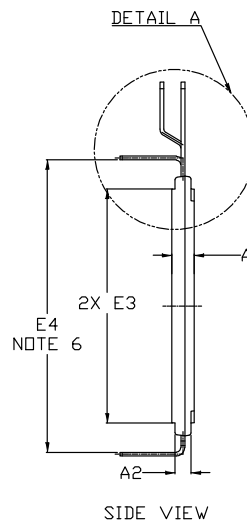
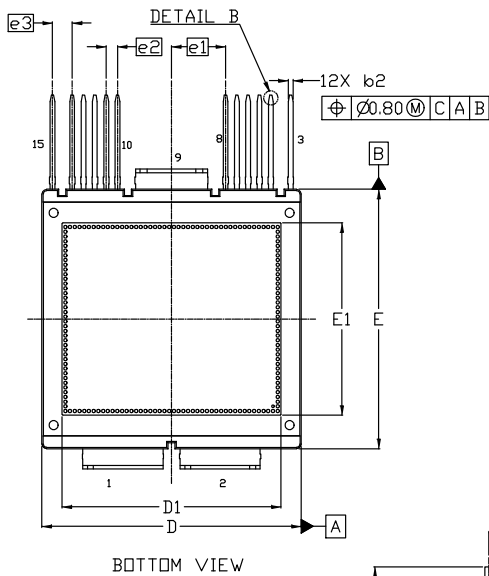
## AHPM15 55x55 CASE MODHS ISSUE B

DATE 06 MAY 2022



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSIONS D & E DO NOT INCLUDE MOLD PROTRUSIONS
4. DIMENSIONS b, b1, b2 DO NOT INCLUDE DAMBAR REMAIN.
5. MARKING AREA.
6. E4 IS FROM INNER LEAD TIP TO INNER LEAD TIP DISTANCE.



DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	4.65	4.70	4.75
A1	10.75	11.05	11.35
A2	3.20	3.40	3.60
A3	1.60	1.95	2.30
A4	5.70	6.00	6.30
b	16.90	17.00	17.10
b1	15.20	15.30	15.40
b2	0.90	1.00	1.10
b3	0.50 REF		
c	0.70	0.80	0.90
D	54.80	55.00	55.20
D1	45.80	46.80	47.80
D2	50.50	51.20	51.90

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
E	54.80	55.00	55.20
E1	40.20	41.20	42.20
E2	29.80	30.50	31.20
E3	49.40	49.60	49.80
E4	61.60	62.00	62.40
e	10.30 BSC		
e1	11.45 BSC		
e2	2.40 BSC		
e3	4.20 BSC		
e4	4.50 BSC		
F	6.45	6.50	6.55
L	19.60	20.00	20.40
L1	3.10	3.50	3.90
L2	5.70	6.00	6.30
L3	9.70	10.00	10.30
M	10° REF		

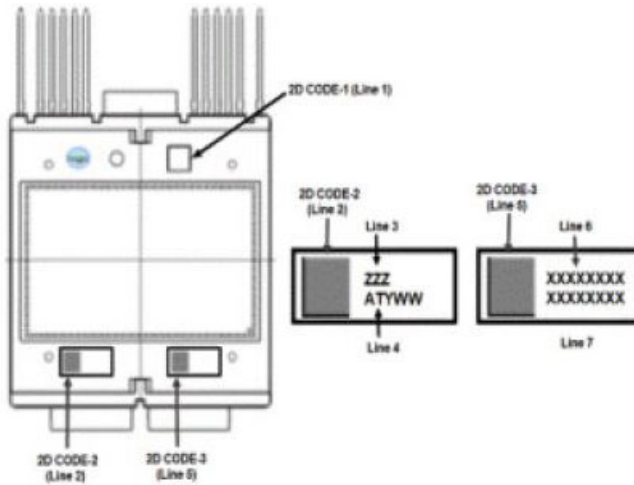
<b>DOCUMENT NUMBER:</b>	<b>98AON32090H</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>AHPM15 55x55</b>	<b>PAGE 1 OF 2</b>

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**AHPM15 55x55**  
**CASE MODHS**  
**ISSUE B**

DATE 06 MAY 2022

**GENERIC  
MARKING DIAGRAM\***



ZZZ = Assembly Lot Code  
AT = Assembly & Test Location  
Y = Year  
WW = Work Week  
XXXX = Specific Device Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

<b>DOCUMENT NUMBER:</b>	<b>98AON32090H</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>AHPM15 55x55</b>	<b>PAGE 2 OF 2</b>

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

---

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at [www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)