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# FDZ8040L

## Integrated Load Switch

### Features

- Optimized for Low-Voltage Core ICs in Portable Systems
- Very Small Package Dimension: WLCSP 0.8 X 0.8 X 0.5 mm<sup>3</sup>
- Current = 1.2 A, V<sub>IN</sub> Max. = 4 V
- Current = 2 A, V<sub>IN</sub> Max. = 4 V (Pulsed)
- R<sub>DS(on)</sub> = 80 mΩ at V<sub>ON</sub> = V<sub>IN</sub> = 4 V
- R<sub>DS(on)</sub> = 85 mΩ at V<sub>ON</sub> = V<sub>IN</sub> = 3.6 V
- R<sub>DS(on)</sub> = 90 mΩ at V<sub>ON</sub> = V<sub>IN</sub> = 3 V
- R<sub>DS(on)</sub> = 360 mΩ at V<sub>ON</sub> = V<sub>IN</sub> = 0.9 V
- R<sub>DS(on)</sub> = 1000 mΩ at V<sub>ON</sub> = V<sub>IN</sub> = 0.8 V
- RoHS Compliant

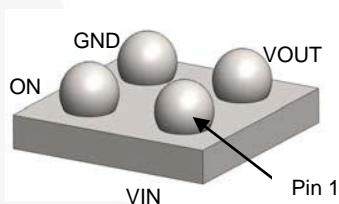


Figure 1. Bottom View

### Description

This device is particularly suited for compact power management in portable applications needing 0.8 V to 4 V input and 1.2 A output current capability. This load switch integrated a level-shifting function that drives a P-channel power MOSFET in a very small 0.8 X 0.8 X 0.5 mm<sup>3</sup> WLCSP package.

### Applications

- Load Switch
- Power Management in Portable Applications

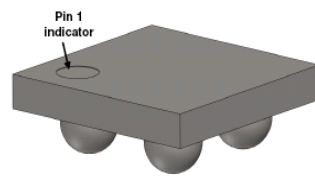


Figure 2. Top View

### Ordering Information

Part Number	Device Mark	Ball Pitch	Operating Temperature Range	Switch	Package	Packing Method
FDZ8040L	ZM	0.4 mm	-40 to 85°C	80 mΩ, P-Channel MOSFET	0.8 x 0.8 x 0.5 mm <sup>3</sup> WLCSP	Tape & Reel

### Typical Application

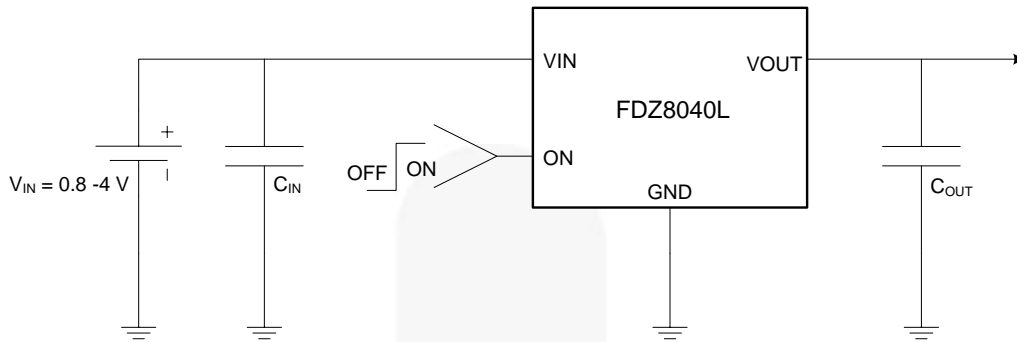


Figure 3. Typical Application

### Block Diagram

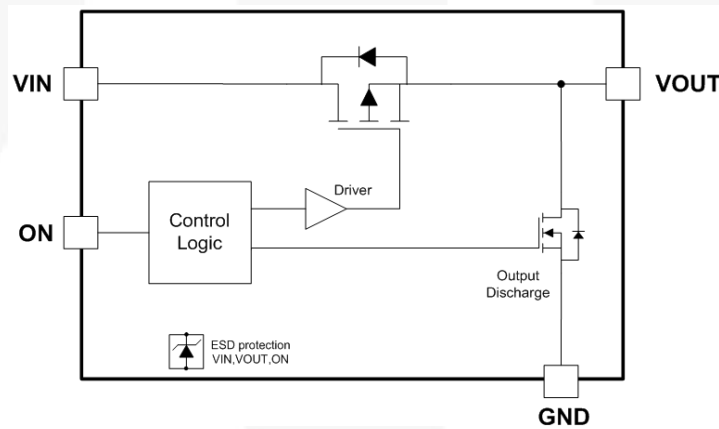


Figure 4. Internal Block Diagram

### Pin Configuration

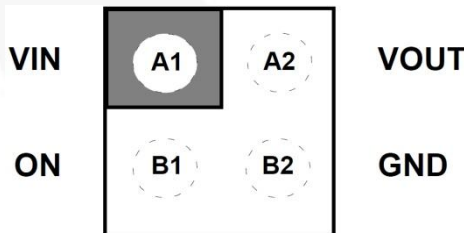


Figure 5. Top View (Bumps Down)

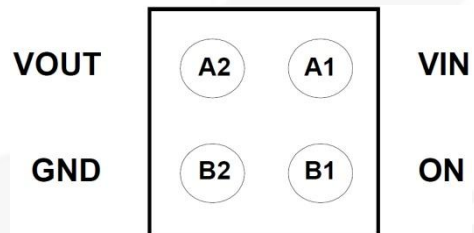


Figure 6. Bottom View (Bumps Up)

### Pin Descriptions

Pin #	Name	Description
A1	VIN	Supply Input: Input to the load switch
A2	VOUT	Switch Output: Output of the load switch
B1	ON	ON/OFF Control Input
B2	GND	Ground

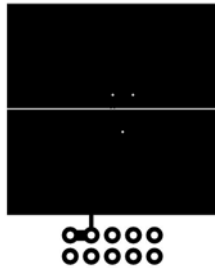
## Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

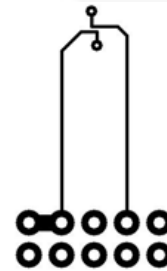
Symbol	Parameter	Min.	Max.	Unit
$V_{IN}$	Voltage on VIN, VOUT, ON to GND	-0.3	4.2	V
$I_{OUT\_C}$	$I_{OUT}$ -Load Current (Continuous) <sup>(1a)</sup>		1.2	A
$I_{OUT\_P}$	$I_{OUT}$ -Load Current (Pulsed)		2	A
$P_D$	Power Dissipation at $T_A = 25^\circ\text{C}$ <sup>(1a)</sup>		0.9	W
$T_A$	Operating Temperature Range	-40	85	$^\circ\text{C}$
$T_{STG}$	Storage Temperature	-65	150	$^\circ\text{C}$
$R\theta_{JA}$	Thermal Resistance, Junction to Ambient <sup>(1a)</sup>		135	$^\circ\text{C}/\text{W}$
ESD	Electrostatic Discharge Capability	Human Body Model, JESD22-A114	8	kV
		Charged Device Model, JESD22-C101	2	

### Notes:

- $R\theta_{JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R\theta_{JC}$  is guaranteed by design, while  $R\theta_{JA}$  is determined by the board design.



- a.  $135^\circ\text{C}/\text{W}$  when mounted on a 1-inch square pad of 2-oz copper.



- b.  $360^\circ\text{C}/\text{W}$  when mounted on a minimum pad of 2-oz copper.

- Pulse test: pulse width < 300  $\mu\text{s}$ ; duty cycle < 2.0%.

## Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit
$V_{IN}$	Voltage on VIN Pin	0.8	4.0	V
$V_{ON}$	Voltage on ON Pin	0.7	4.0	V
$T_A$	Operating Temperature Range	1 V to 4 V	-40	85
		0.8 V to 4 V	-10	

## Electrical Characteristics

$T_J = 25^\circ\text{C}$  and  $V_{IN} = 1.8\text{ V}$ , unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{IN}$	Operation Voltage		0.8		4.0	V
$V_{IL}$	ON Input Logic Low Voltage	$1.6\text{ V} \leq V_{IN} \leq 4.0\text{ V}$			0.35	V
		$0.8\text{ V} \leq V_{IN} \leq 1.6\text{ V}$			0.25	
$V_{IH}$	ON Input Logic High Voltage	$1.6\text{ V} \leq V_{IN} \leq 4.0\text{ V}$	1.0			V
		$0.8\text{ V} \leq V_{IN} \leq 1.6\text{ V}$	0.7			
$I_Q$	Quiescent Current	$I_{OUT} = 0\text{ mA}$ , $V_{IN} = V_{ON} = 1.8\text{ V}$			2.1	$\mu\text{A}$
$I_{Q(off)}$	Off Supply Current	$I_{OUT} = 0\text{ mA}$ , $V_{IN} = 1.8\text{ V}$ , $V_{ON} = \text{GND}$			1	$\mu\text{A}$
$I_{SD(off)}$	Off Switch Current	$V_{ON} = \text{GND}$ , $V_{OUT} = 0\text{ V}$ , $V_{IN} = 1.8\text{ V}$			100	nA
$I_{ON}$	ON Input Leakage	$V_{ON} = V_{IN}$ or GND			1	$\mu\text{A}$
$R_{PD}$	Output Discharge Pull-Down Resistance			200		$\Omega$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{ON} = V_{IN} = 4\text{ V}$ , $I_{OUT} = 300\text{ mA}$		50	80	m $\Omega$
		$V_{ON} = V_{IN} = 3.6\text{ V}$ , $I_{OUT} = 300\text{ mA}$		51	85	
		$V_{ON} = V_{IN} = 3\text{ V}$ , $I_{OUT} = 300\text{ mA}$		54	90	
		$V_{ON} = 0.7\text{ V}$ , $V_{IN} = 1.6\text{ V}$ , $I_{OUT} = 300\text{ mA}$		73	110	
		$V_{ON} = 0.7\text{ V}$ , $V_{IN} = 1\text{ V}$ , $I_{OUT} = 300\text{ mA}$		140	309	
		$V_{ON} = V_{IN} = 0.9\text{ V}$ , $I_{OUT} = 10\text{ mA}$		186	360	
		$V_{ON} = V_{IN} = 0.8\text{ V}$ , $I_{OUT} = 10\text{ mA}$		348	1000	
		$V_{ON} = V_{IN} = 0.9\text{ V}$ , $I_{OUT} = 10\text{ mA}$ , $T_J = 10 \sim 85^\circ\text{C}$		194	370	
		$V_{ON} = V_{IN} = 0.8\text{ V}$ , $I_{OUT} = 10\text{ mA}$ , $T_J = 10 \sim 85^\circ\text{C}$		268	750	
		$V_{IN} = 3.6\text{ V}$ , $I_{OUT} = 300\text{ mA}$ , $T_J = 85^\circ\text{C}$		59	102	

## Switching Characteristics

Symbol	Parameter	Test Conditions	Typical	Unit
$t_{d(on)}$	Turn-On Delay Time	$V_{IN} = 1.6\text{ V}, V_{ON} = 0.7\text{ V}, C_L = 1\ \mu\text{F}, R_L = 500\ \Omega$	22	$\mu\text{s}$
$t_r$	Turn-On Rise Time		23	$\mu\text{s}$
$t_{d(off)}$	Turn-Off Delay Time		109	$\mu\text{s}$
$t_f$	Turn-Off Fall Time		285	$\mu\text{s}$
$t_{d(on)}$	Turn-On Delay Time	$V_{IN} = 1\text{ V}, V_{ON} = 1.8\text{ V}, C_L = 1\ \mu\text{F}, R_L = 500\ \Omega$	37	$\mu\text{s}$
$t_r$	Turn-On Rise Time		35	$\mu\text{s}$
$t_{d(off)}$	Turn-Off Delay Time		112	$\mu\text{s}$
$t_f$	Turn-Off Fall Time		332	$\mu\text{s}$
$t_{d(on)}$	Turn-On Delay Time	$V_{IN} = 1.8\text{ V}, V_{ON} = 1.8\text{ V}, C_L = 1\ \mu\text{F}, R_L = 500\ \Omega$	20	$\mu\text{s}$
$t_r$	Turn-On Rise Time		22	$\mu\text{s}$
$t_{d(off)}$	Turn-Off Delay Time		122	$\mu\text{s}$
$t_f$	Turn-Off Fall Time		296	$\mu\text{s}$
$t_{d(on)}$	Turn-On Delay Time	$V_{IN} = 2.5\text{ V}, V_{ON} = 1.8\text{ V}, C_L = 1\ \mu\text{F}, R_L = 500\ \Omega$	15	$\mu\text{s}$
$t_r$	Turn-On Rise Time		19	$\mu\text{s}$
$t_{d(off)}$	Turn-Off Delay Time		160	$\mu\text{s}$
$t_f$	Turn-Off Fall Time		295	$\mu\text{s}$
$t_{d(on)}$	Turn-On Delay Time	$V_{IN} = 3.3\text{ V}, V_{ON} = 1.8\text{ V}, C_L = 1\ \mu\text{F}, R_L = 500\ \Omega$	13	$\mu\text{s}$
$t_r$	Turn-On Rise Time		18	$\mu\text{s}$
$t_{d(off)}$	Turn-Off Delay Time		193	$\mu\text{s}$
$t_f$	Turn-Off Fall Time		305	$\mu\text{s}$
$t_{d(on)}$	Turn-On Delay Time	$V_{IN} = 0.8\text{ V}, V_{ON} = 0.8\text{ V}, C_L = 1\ \mu\text{F}, R_L = 500\ \Omega$	53	$\mu\text{s}$
$t_r$	Turn-On Rise Time		56	$\mu\text{s}$
$t_{d(off)}$	Turn-Off Delay Time		143	$\mu\text{s}$
$t_f$	Turn-Off Fall Time		532	$\mu\text{s}$
$t_{d(on)}$	Turn-On Delay Time	$V_{IN} = 0.9\text{ V}, V_{ON} = 0.9\text{ V}, C_L = 1\ \mu\text{F}, R_L = 500\ \Omega$	51	$\mu\text{s}$
$t_r$	Turn-On Rise Time		54	$\mu\text{s}$
$t_{d(off)}$	Turn-Off Delay Time		148	$\mu\text{s}$
$t_f$	Turn-Off Fall Time		525	$\mu\text{s}$

## Typical Performance Characteristics

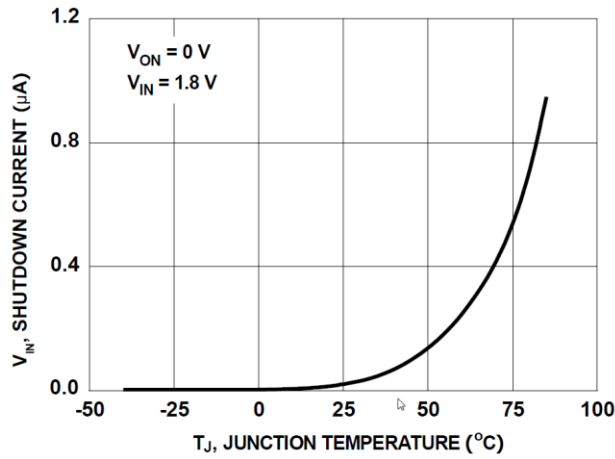


Figure 7. Shutdown Current vs. Temperature

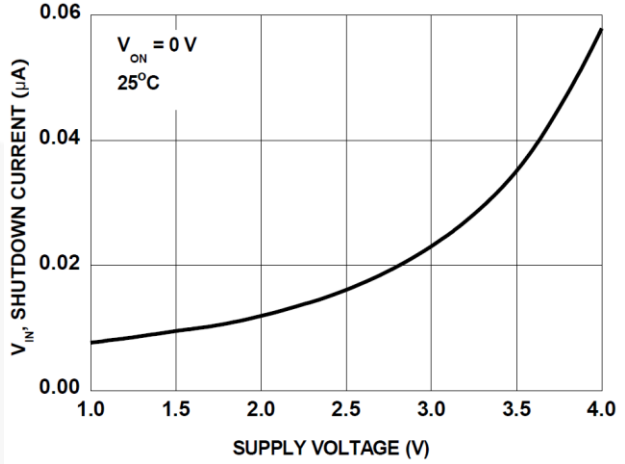


Figure 8. Shutdown Current vs. Supply Voltage

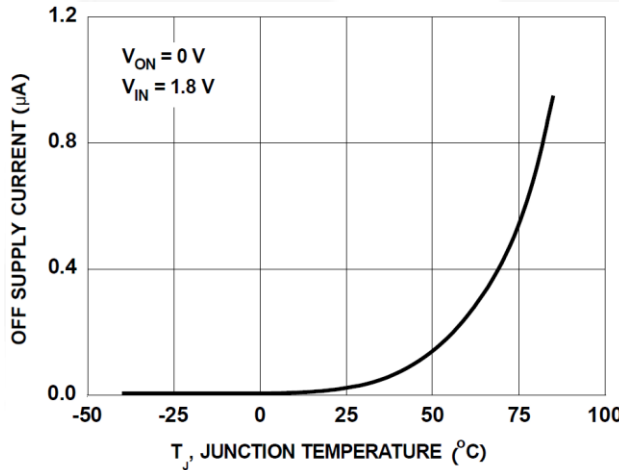


Figure 9. Off Supply Current vs. Temperature

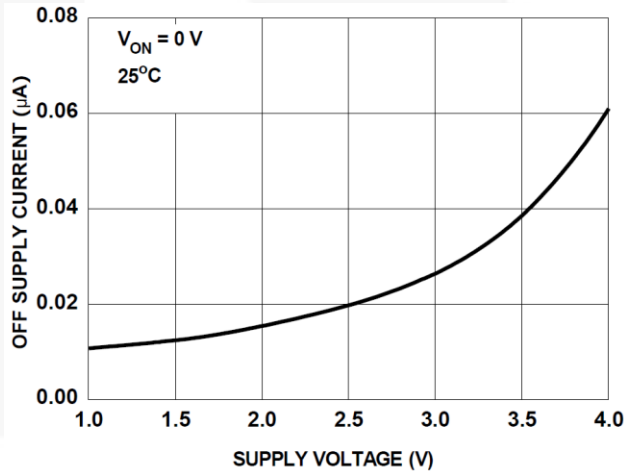


Figure 10. Off Supply Current vs. Supply Voltage

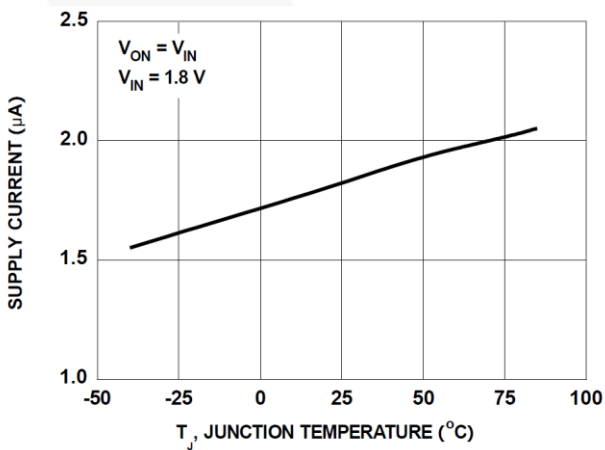


Figure 11. Quiescent Current vs. Temperature

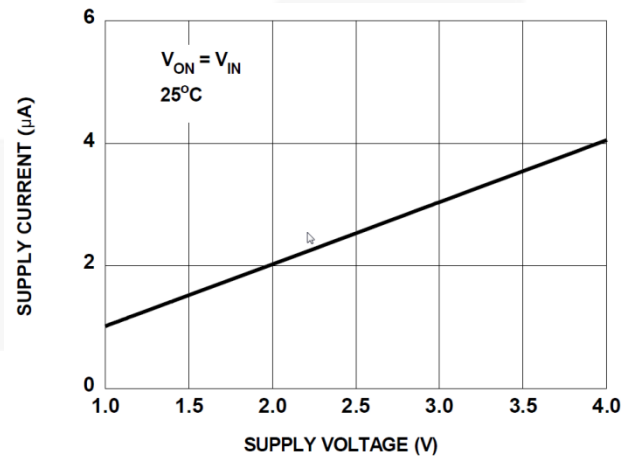


Figure 12. Quiescent Current vs. Supply Voltage

### Typical Performance Characteristics

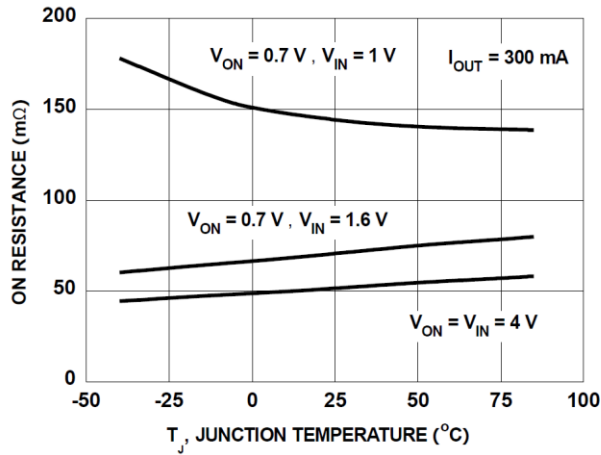


Figure 13.  $R_{ON}$  vs. Temperature

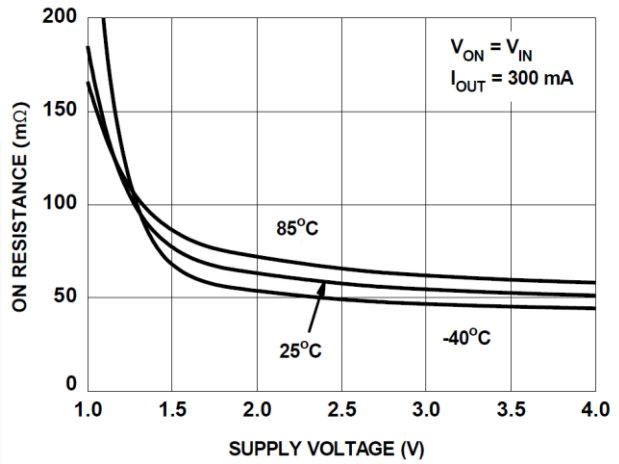


Figure 14.  $R_{ON}$  vs. Supply Voltage

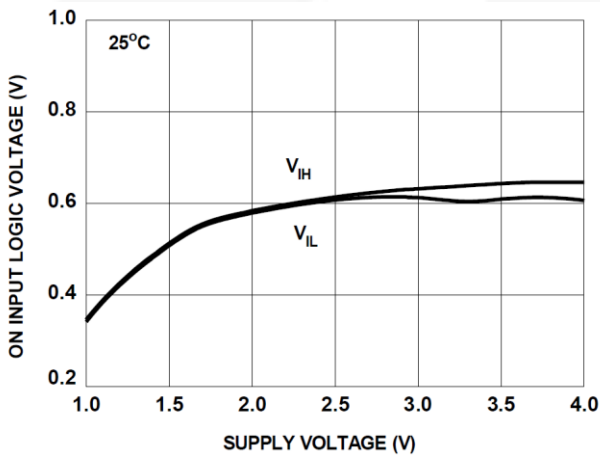


Figure 15. ON-Pin Threshold vs.  $V_{IN}$

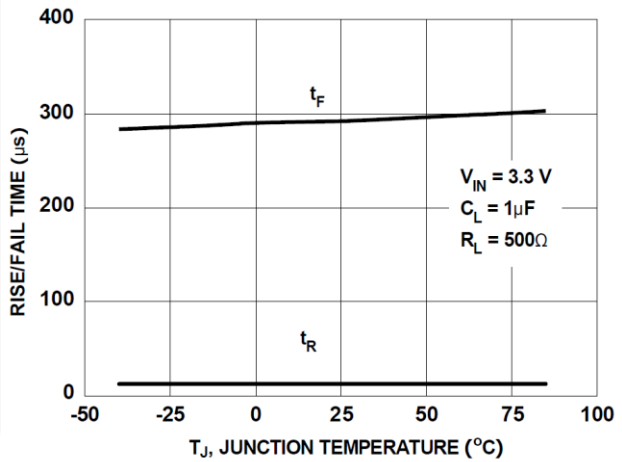


Figure 16.  $V_{OUT}$  Rise and Fall Time vs. Temperature at  $R_L=500\Omega$

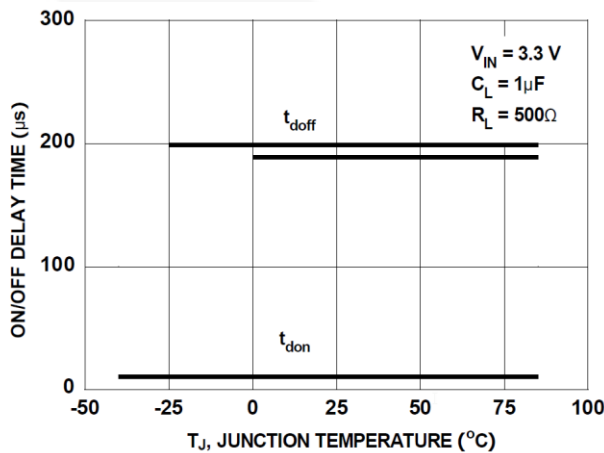


Figure 17.  $V_{OUT}$  Turn-On and Turn-Off Delay vs. Temperature at  $R_L=500\Omega$

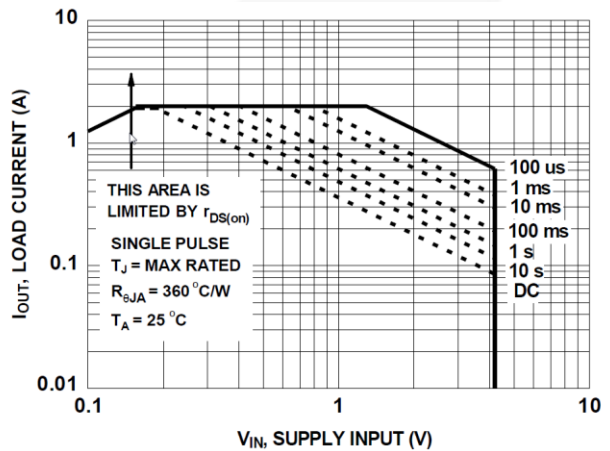


Figure 18. Forward Bias Safe Operation Area



## Typical Performance Characteristics

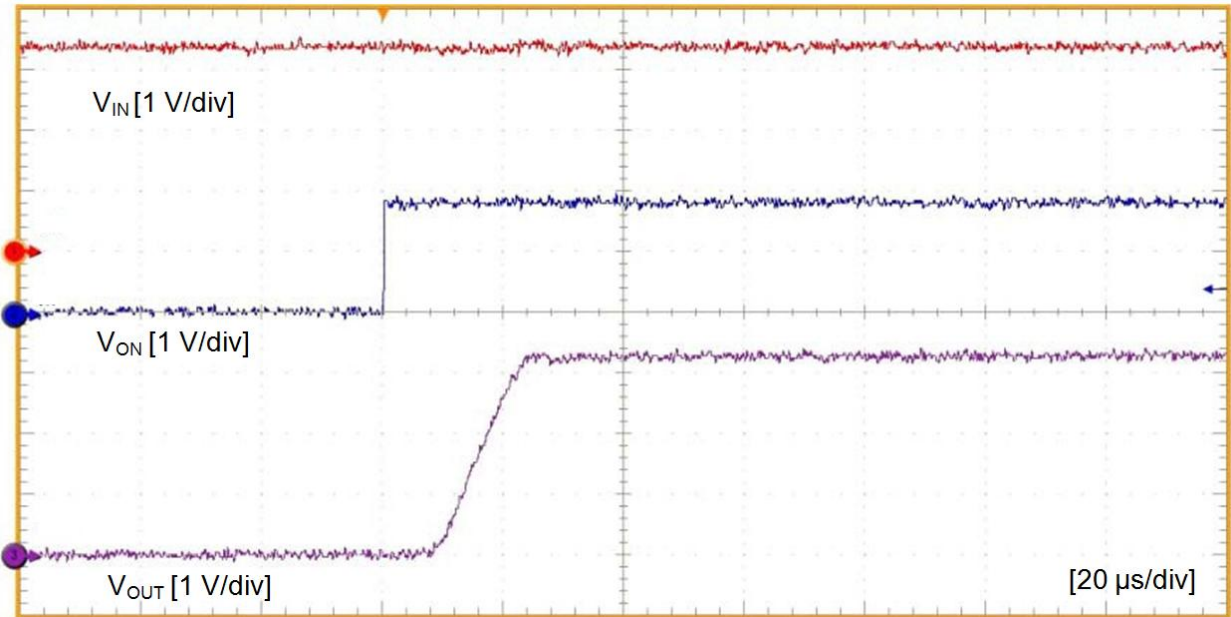


Figure 19. Turn-On Response ( $V_{IN} = 3.3 \text{ V}$ ,  $C_{OUT} = 1 \mu\text{F}$ ,  $R_L = 500 \Omega$ )

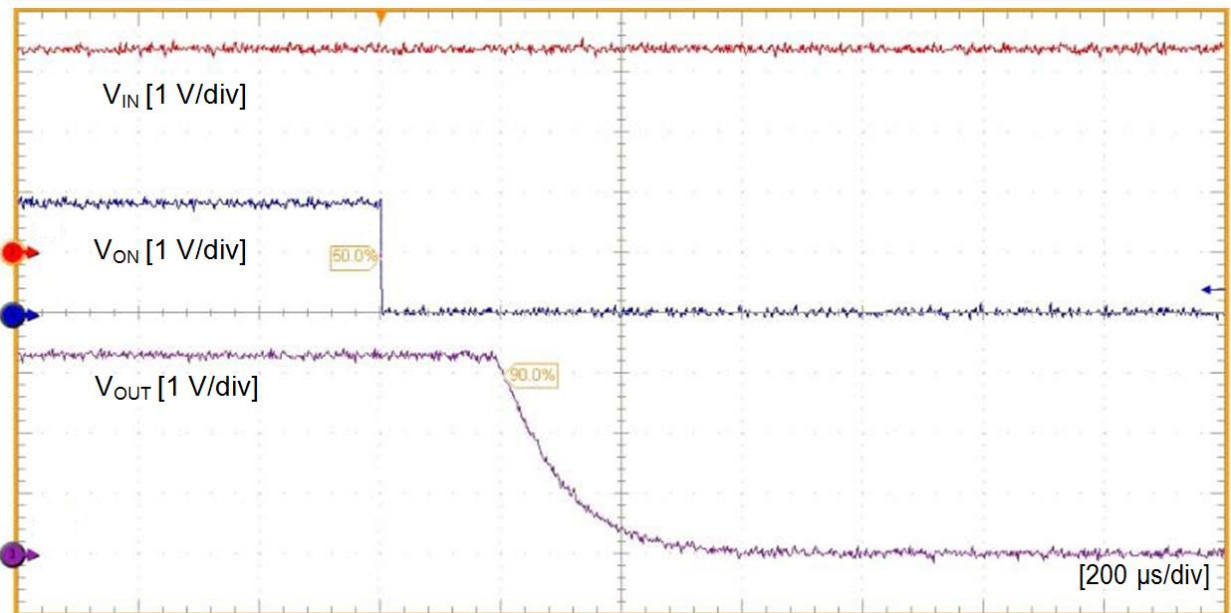


Figure 20. Turn-Off Response ( $V_{IN} = 3.3 \text{ V}$ ,  $C_{OUT} = 1 \mu\text{F}$ ,  $R_L = 500 \Omega$ )

## Functional Description

The FDZ8040L is a low- $R_{DS(ON)}$  P-channel load switch packaged in space-saving 0.8 x 0.8 WLCSP.

The core of the device is an 80 m $\Omega$  P-channel MOSFET capable of functioning over a wide input operating range

of 0.8-4 V. The ON pin, an active HIGH TTL-compatible input that supports input as low as 0.7 V, controls the state of the switch.

## Applications Information

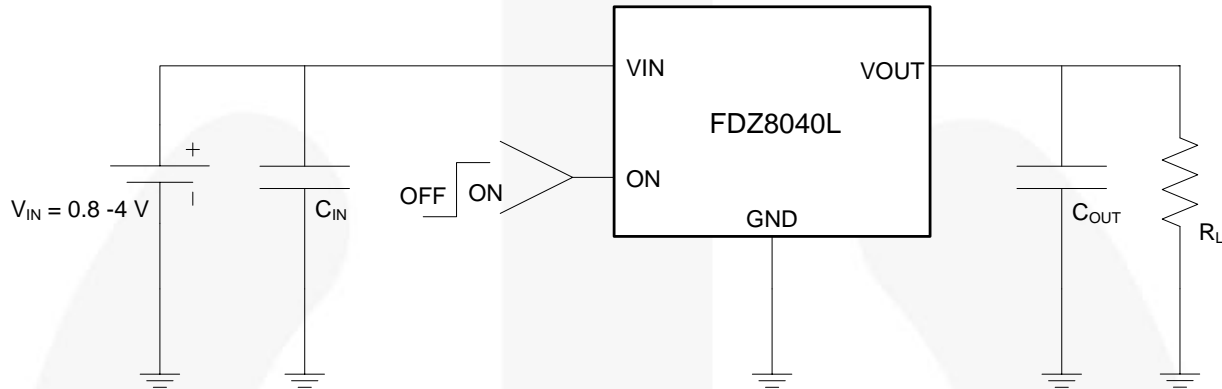


Figure 21. Typical Application

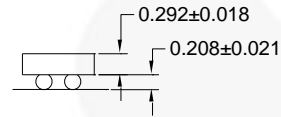
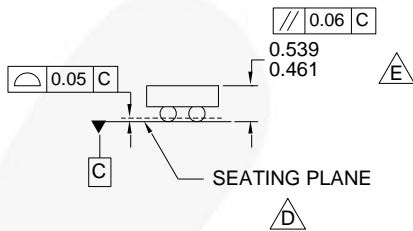
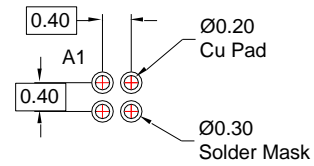
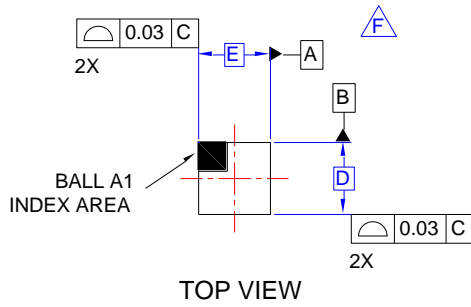
### Input Capacitor

To reduce device inrush current effect, a 0.1  $\mu\text{F}$  ceramic capacitor,  $C_{IN}$ , is recommended close to the VIN pin. A higher value of  $C_{IN}$  can be used to further reduce the voltage drop experienced as the switch is turned on into a large capacitive load.

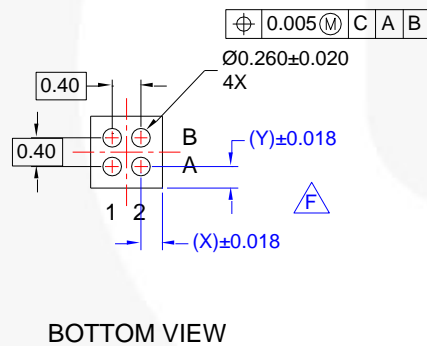
### Output Capacitor

FDZ8040L works without an output capacitor. However, if parasitic board inductance forces  $V_{OUT}$  below GND when switching off, a 0.1  $\mu\text{F}$  capacitor,  $C_{OUT}$ , should be placed between the VOUT and GND pins.

## Physical Dimensions



**SIDE VIEWS**



### NOTES:

- A. NO JEDEC REGISTRATION APPLIES.
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCE PER ASME Y14.5M, 1994.
- D. DATUM C IS DEFINED BY THE SPHERICAL CROWNS OF THE BALLS.
- E. PACKAGE NOMINAL HEIGHT IS 500 MICRONS ±39 MICRONS (461-539 MICRONS).
- F. FOR DIMENSIONS D, E, X, AND Y SEE PRODUCT DATASHEET.
- G. DRAWING FILNAME: MKT-UC004AFrev1.

**Figure 22. 4-Ball, WLCSP, 2 X 2 Array, 0.4 mm Pitch, 250 µm Ball**

## Product-Specific Dimensions

Product	D	E	X	Y
FDZ8040L	0.8 ±0.03 mm	0.8 ±0.03 mm	0.21 mm	0.21 mm






Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

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| Build it Now™   | GreenBridge™                                   | QFET®   | TinyCalc™   |
| CorePLUS™   | Green FPS™                                     | QS™   | TinyLogic®  |
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| Fairchild Semiconductor®  | MillerDrive™                                   | SuperSOT™-3   | UniFET™   |
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