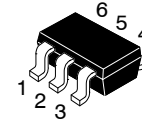


N-Channel JFET

25 V, 20 to 40 mA, 40 mS, Dual CPH6

CPH6904



CPH6
CASE 318BD

特長

- CPH6 にJ-FET を2 素子内蔵した複合タイプであり、実装基板効率が大幅にアップできる
- CPH6904 は、CPH3910 相当のチップを2 個同一ケース内に収容したものである
- これは鉛フリーのデバイスです

製品と外形に伴う情報

- パッケージ名 : CPH6
- JEITA, JEDEC : SC-74, SOT-26, SOT-457
- 最小梱包単位 : 3,000 pcs./reel

絶対最大定格 (T_A = 25°C)

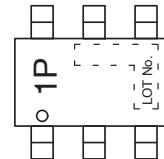
記号	項目	条件	定格値	単位
V _{DSX}	ドレイン・ソース電圧		25	V
V _{GDS}	ゲート・ドレイン電圧		-25	V
I _G	ゲート電流		10	mA
I _D	ドレイン電流		50	mA
P _D	許容損失	1 unit	400	mW
P _T	全損失		700	mW
T _{ch}	チャネル温度		150	°C
T _{stg}	保存周囲温度		-55 ~ +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

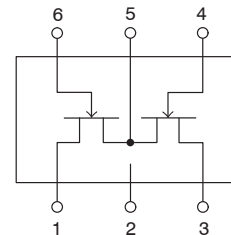
(参考訳)

最大定格を超えるストレスは、デバイスにダメージを与える危険性があります。これらの定格値を超えた場合は、デバイスの機能性を損ない、ダメージが生じたり、信頼性に影響を及ぼす危険性があります。

MARKING DIAGRAM



ELECTRICAL CONNECTION



- 1 : Drain 1
- 2 : NC
- 3 : Drain 2
- 4 : Gate 2
- 5 : Source 1 / Source 2
- 6 : Gate 1

ORDERING INFORMATION

Device	Package	Shipping [†]
CPH6904-TL-E	CPH6 (Pb-Free)	3 000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, [BRD8011/D](#).

電気的特性 (T_A = 25°C)

記号	項目	条件	最小	標準値	最大	単位
V _{(BR)GDS}	ゲート・ドレイン降伏電圧	I _G = -10 μA, V _{DS} = 0 V	-25			V
I _{GSS}	ゲートしゃ断電流	V _{GS} = -10 V, V _{DS} = 0 V			-1.0	nA
V _{GS(off)}	ゲート・ソースしゃ断電圧	V _{DS} = 5 V, I _D = 100 μA	-0.6	-1.2	-1.8	V
I _{DSS}	ドレイン電流	V _{DS} = 5 V, V _{GS} = 0 V	20.0		40.0	mA
y _{fs}	順伝達アドミタンス	V _{DS} = 5 V, V _{GS} = 0 V, f = 1 kHz	30	40		mS
C _{iss}	入力容量	V _{DS} = 5 V, V _{GS} = 0 V, f = 1 MHz		6.0		pF
C _{rss}	帰還容量	V _{DS} = 5 V, V _{GS} = 0 V, f = 1 MHz		2.3		pF
N _F	雑音指数	V _{DS} = 5 V, V _{GS} = 0 V, f = 100 MHz		2.1	2.8	dB

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

(参考訳)

製品パラメータは、特別な記述が無い限り、記載されたテスト条件に対する電気的特性で示しています。異なる条件下で製品動作を行った時には、電気的特性で示している特性を得られない場合があります。

電氣的特性の代表例

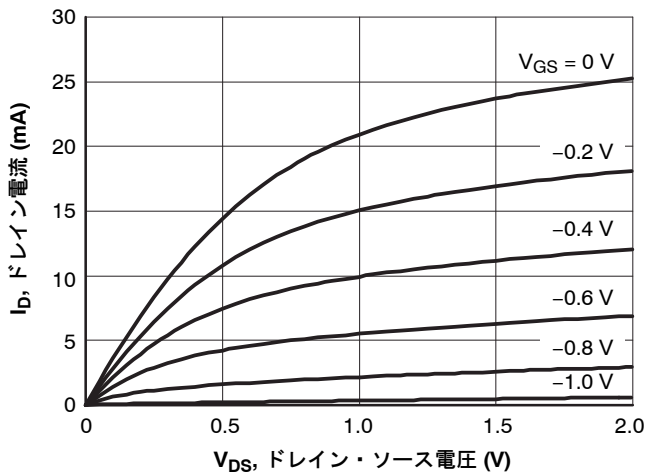


図 1. $I_D - V_{DS}$

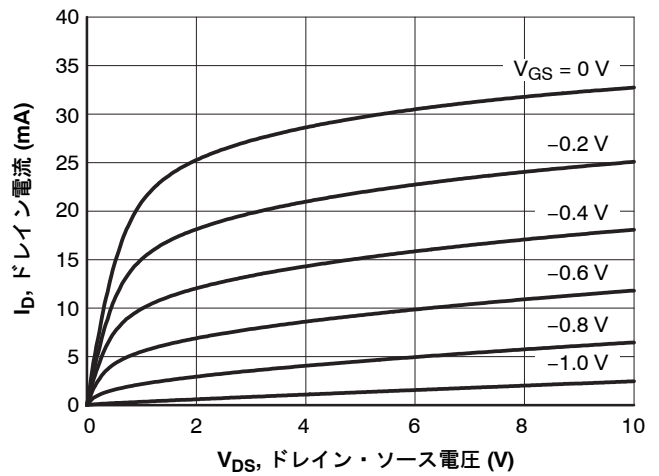


図 2. $I_D - V_{DS}$

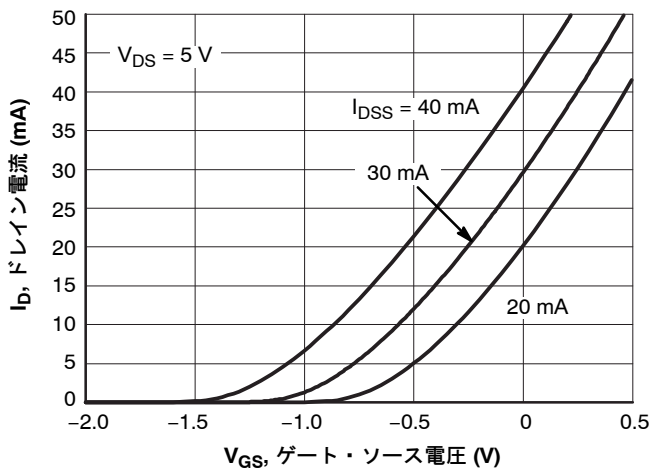


図 3. $I_D - V_{GS}$

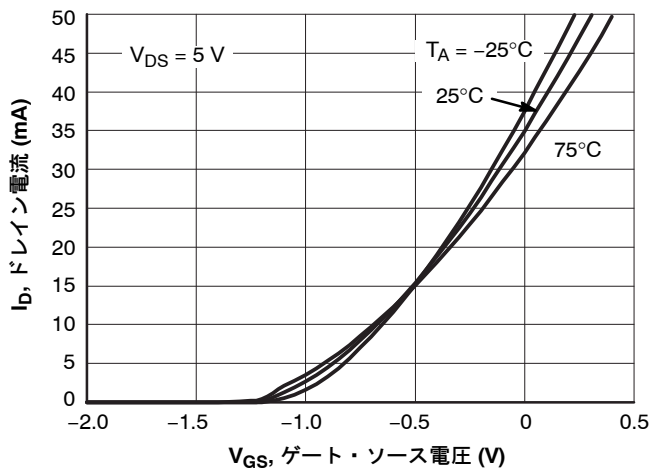


図 4. $I_D - V_{GS}$

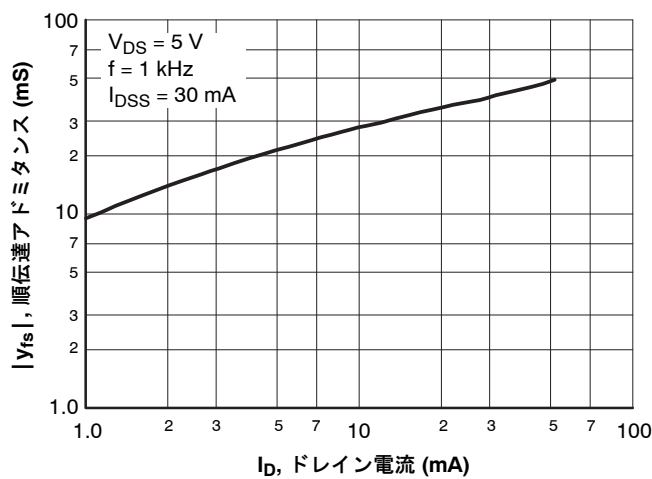


図 5. $|Y_{fs}| - I_D$

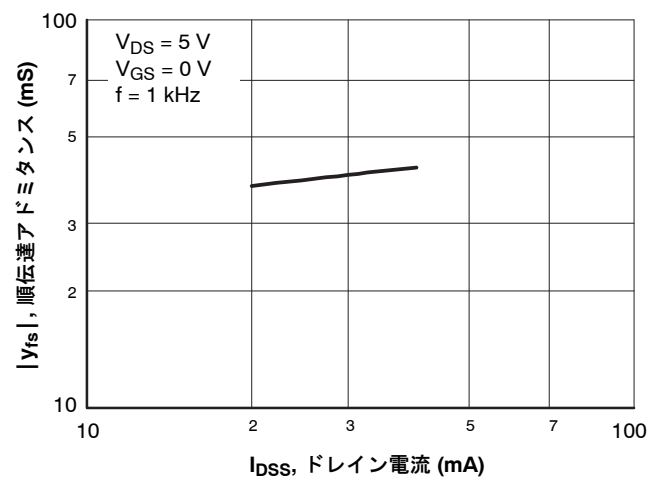


図 6. $|Y_{fs}| - I_{DSS}$

電氣的特性の代表例 (つづき)

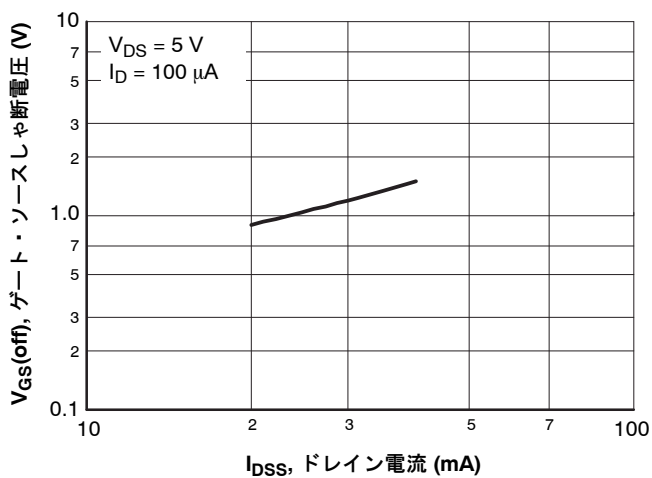


図 7. $V_{GS(off)} - I_{DSS}$

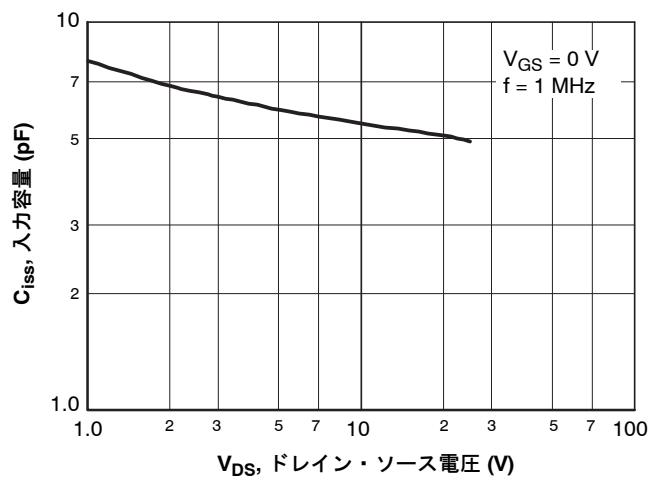


図 8. $C_{iss} - V_{DS}$

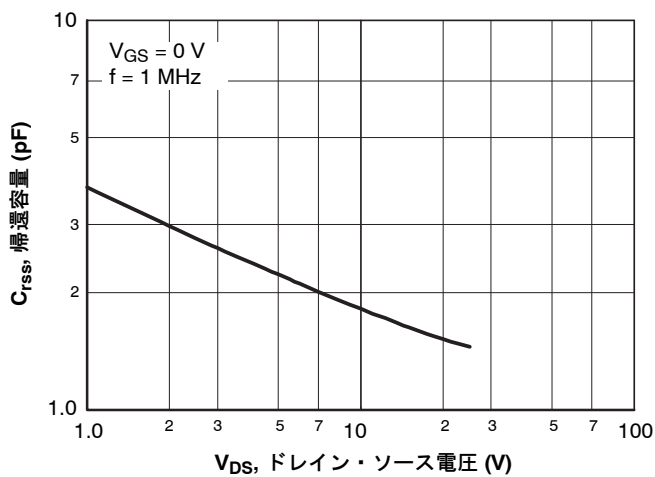


図 9. $C_{rss} - V_{DS}$

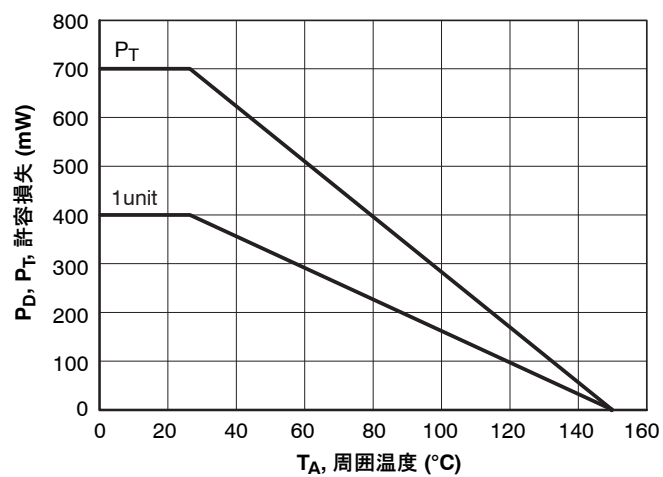
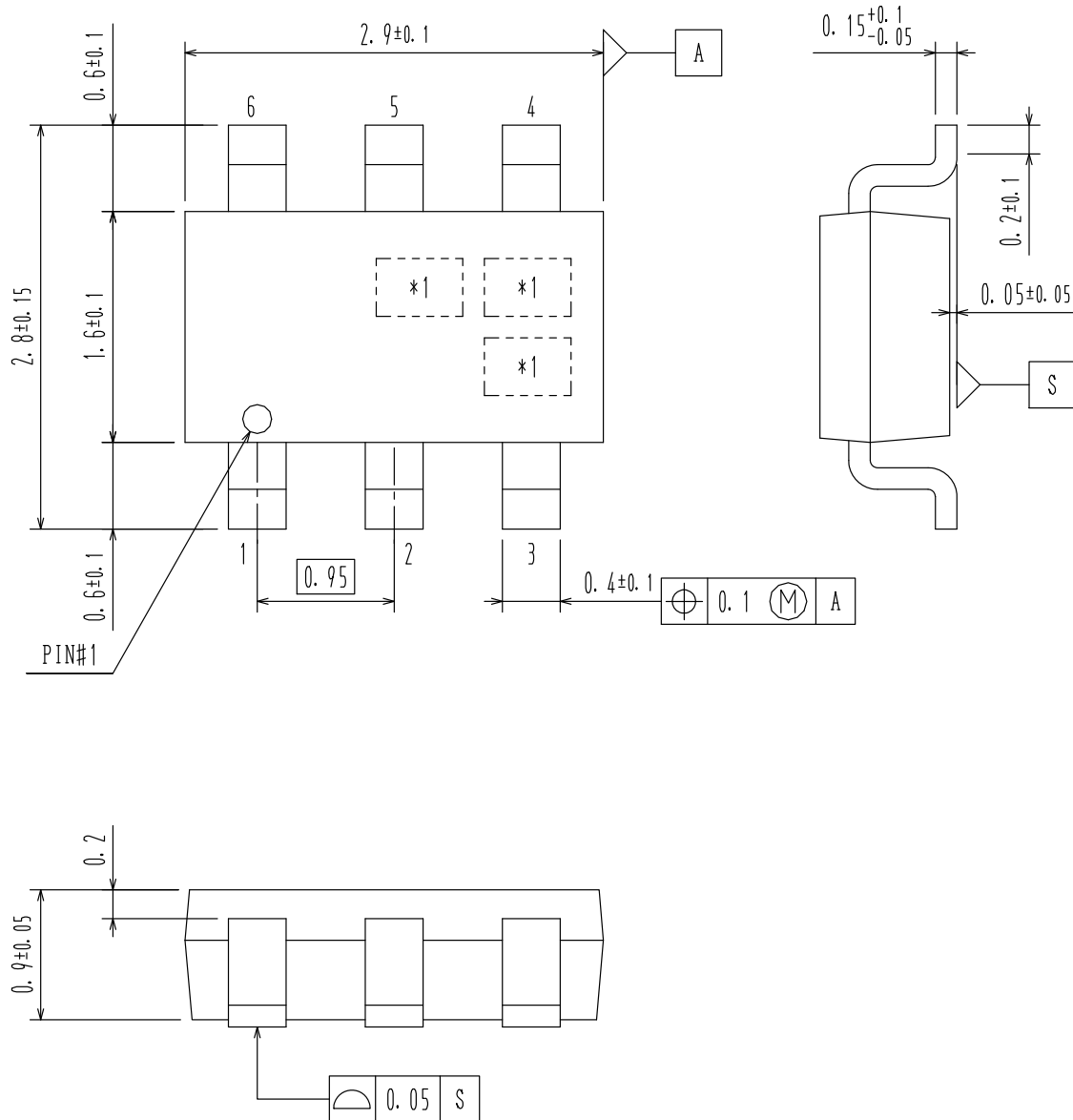


図 10. $P_D, P_T - T_A$

MECHANICAL CASE OUTLINE
PACKAGE DIMENSIONS

CPH6
CASE 318BD
ISSUE O

DATE 30 NOV 2011



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