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JFET VHF/UHF Amplifier

N–Channel – Depletion

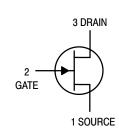
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain–Source Voltage	V _{DS}	25	Vdc
Drain–Gate Voltage	V _{DG}	25	Vdc
Gate-Source Voltage	V _{GS}	25	Vdc
Drain Current	I _D	100	mAdc
Forward Gate Current	I _{G(f)}	10	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	350 2.8	mW mW/°C
Storage Channel Temperature Range	T _{stg}	-65 to +150	°C



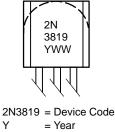
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MARKING DIAGRAM



WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping
2N3819	TO-92 5000 Units/E	

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic			Min	Тур	Max	Unit
OFF CHARACTERISTICS					•	
Gate–Source Breakdown Voltage $(I_G = 1.0 \ \mu Adc, \ V_{DS} = 0)$		V _{(BR)GSS}	25	-	-	Vdc
Gate-Source (V _{DS} = 15 Vdc, I _D = 200 µAdc)		V _{GS}	0.5	-	7.5	Vdc
Gate–Source Cutoff Voltage (V _{DS} = 15 Vdc, I _D = 10 nAdc)		V _{GS(off)}	-	-	-8.0	Vdc
Gate Reverse Current ($V_{GS} = 15 \text{ Vdc}, V_{DS} = 0$)		I _{GSS}	-	-	210	nAdc
ON CHARACTERISTICS					-	
Zero–Gate–Voltage Drain Curren $(V_{DS} = 15 \text{ Vdc}, V_{GS} = 0)$	t	I _{DSS}	2.0	-	20	mAdc
SMALL-SIGNAL CHARACTE	RISTICS				•	•
Forward Transfer Admittance	(V _{DS} = 15 Vdc, V _{GS} = 0, f = 1.0 kHz)	Y _{fs}	3.0	-	6.5	mmhos
Output Admittance	$(V_{DS} = 15 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ kHz})$	Y _{os}	-	40	-	μmhos
Forward Transfer Admittance	$(V_{DS} = 15 \text{ Vdc}, V_{GS} = 0, f = 200 \text{ MHz})$	Y _{fs}	-	5.6	-	mmhos
Reverse Transfer Admittance	$(V_{DS} = 15 \text{ Vdc}, V_{GS} = 0, f = 200 \text{ MHz})$	Y _{rs}	-	1.0	-	mmhos
Input Capacitance	$(V_{DS} = 20 \text{ Vdc}, -V_{GS} = 1.0 \text{ Vdc})$	C _{iss}	-	3.0	-	pF
Reverse Transfer Capacitance	$(V_{DS} = 20 \text{ Vdc}, -V_{GS} = 1.0 \text{ Vdc}, \text{ f} = 1.0 \text{ MHz})$	C _{rss}	-	0.7	-	pF
Output Capacitance	$(V_{DS} = 20 \text{ Vdc}, -V_{GS} = 1.0 \text{ Vdc}, \text{ f} = 1.0 \text{ MHz})$	C _{oss}	-	0.9	-	pF
Cut-off Frequency (Note 1)	(V _{DS} = 15 Vdc, V _{GS} = 0)	F _(Yfs)	_	700	-	MHz

1. The frequency at which g_{fs} is 0.7 of its value at 1 kHz.

COMMON SOURCE CHARACTERISTICS ADMITTANCE PARAMETERS

 $(V_{DS} = 15 \text{ Vdc}, T_{channel} = 25^{\circ}\text{C})$

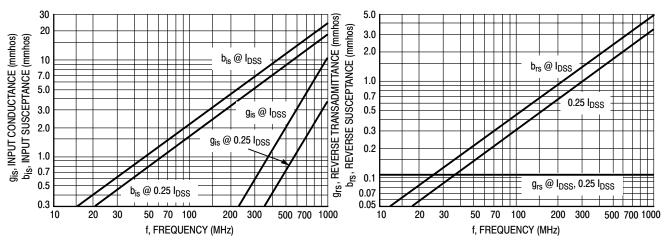




Figure 2. Reverse Transfer Admittance (y_{rs})

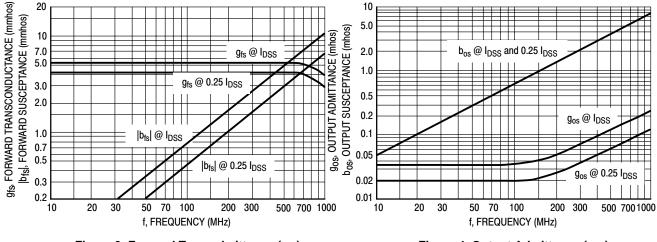
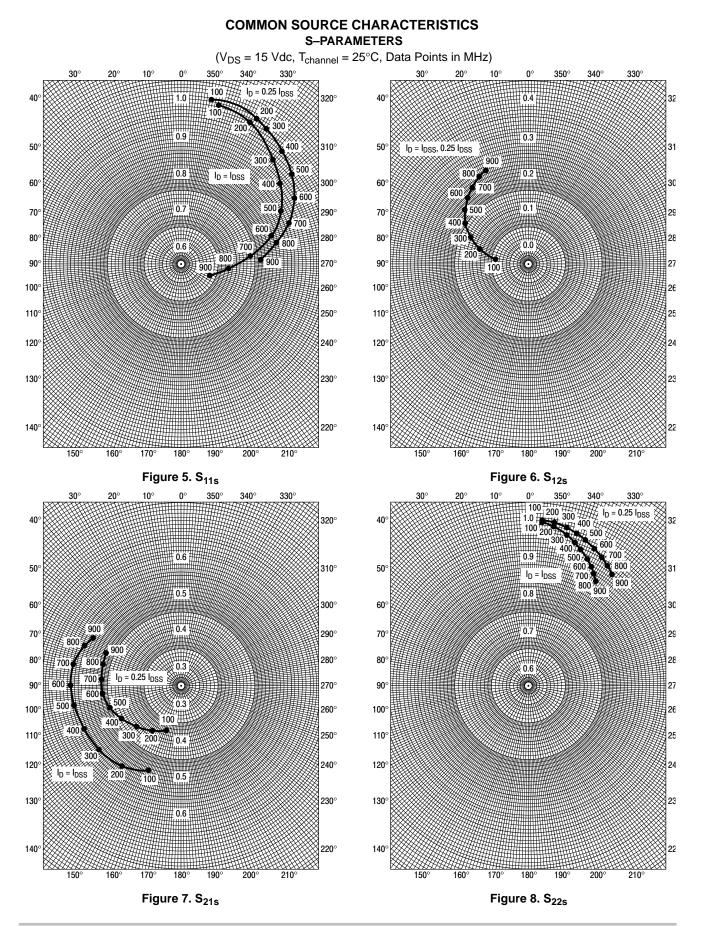


Figure 3. Forward Transadmittance (y_{fs})

Figure 4. Output Admittance (yos)



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(V_{DG} = 15 Vdc, T_{channel} = 25°C)

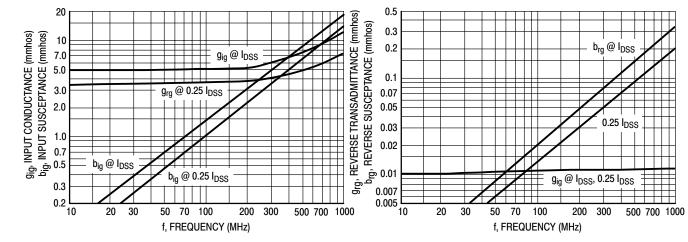
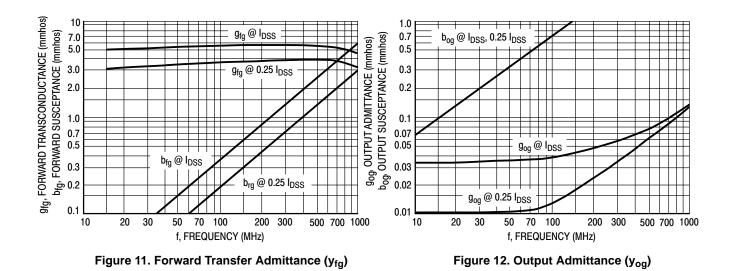


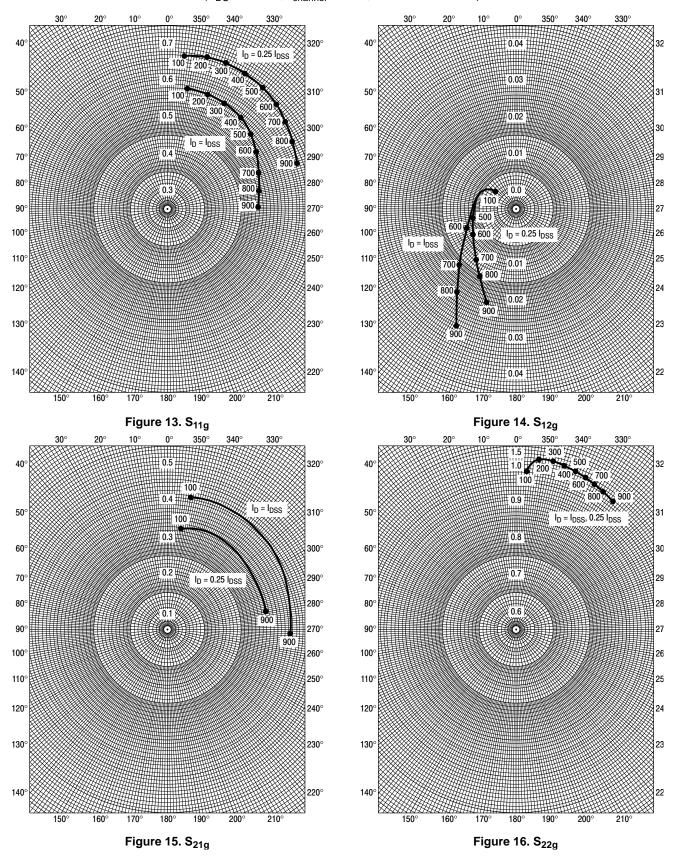
Figure 9. Input Admittance (y_{ig})

Figure 10. Reverse Transfer Admittance (yrg)



COMMON GATE CHARACTERISTICS S-PARAMETERS

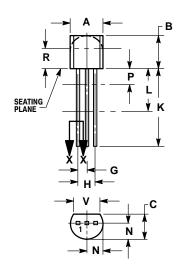
(V_{DS} = 15 Vdc, T_{channel} = 25°C, Data Points in MHz)



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PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AL**





NOTES:
DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
CONTROLLING DIMENSION: INCH.
CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
К	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
V	0.135		3.43	

STYLE 22: PIN 1. SOURCE 2. GATE 3. DRAIN

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